

- Implement functions X and Y with two decoders, two 8-input NAND gates and inverters if needed. Draw all connections and input signals.

	ABCD	X	Y
0	0000	1	0
1	0001	1	0
2	0010	0	0
3	0011	0	0
4	0100	1	1
5	0101	0	1
6	0110	0	0
7	0111	1	1
8	1000	1	0
9	1001	1	1
10	1010	0	0
11	1011	0	0
12	1100	0	1
13	1101	0	1
14	1110	0	1
15	1111	1	1

