

# MA/CSSE 474

## Theory of Computation

TM Design  
Macro Language  
D and SD

## Your Questions?

- Previous class days' material
- Reading Assignments
- HW 13 problems
- Anything else

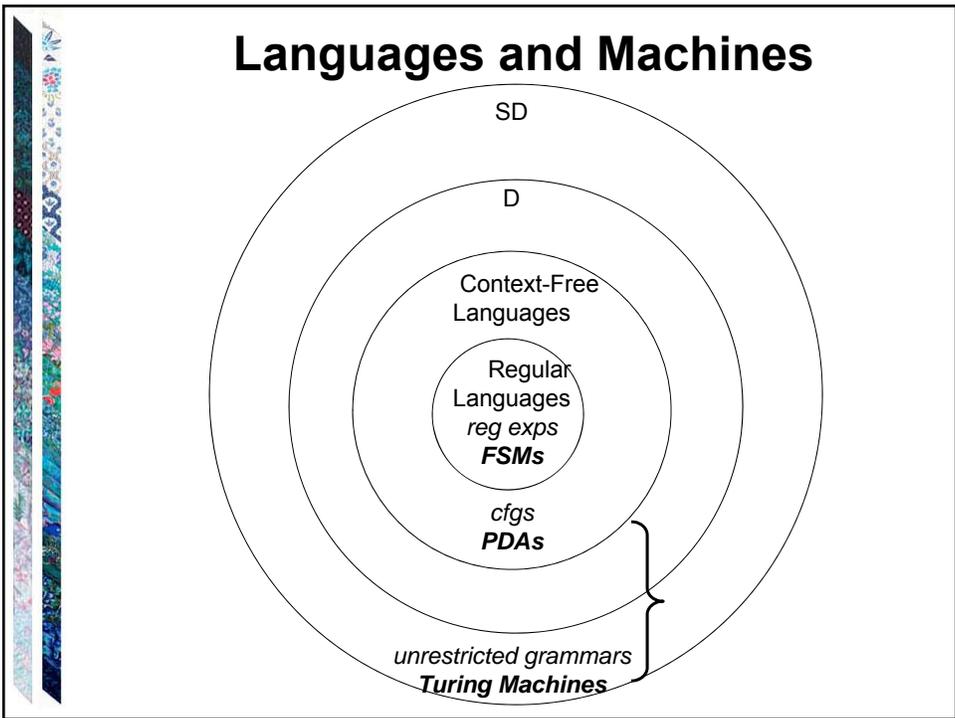
I have included some slides online that we will not have time to do in class, but may be helpful to you anyway.



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# TURING MACHINES



## Turing Machines

Finite State Controller  
 $s, q_1, q_2, \dots, h_1, h_2$

At each step, the machine must:

- choose its next state,
- write on the current square, and
- move left or right.

## TM Formal Definition

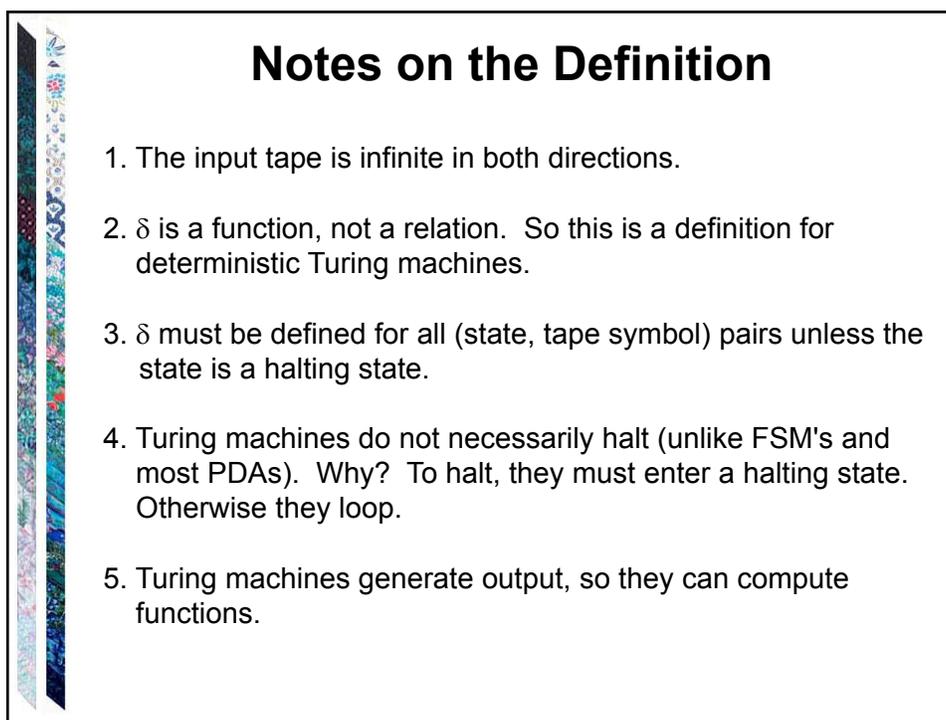
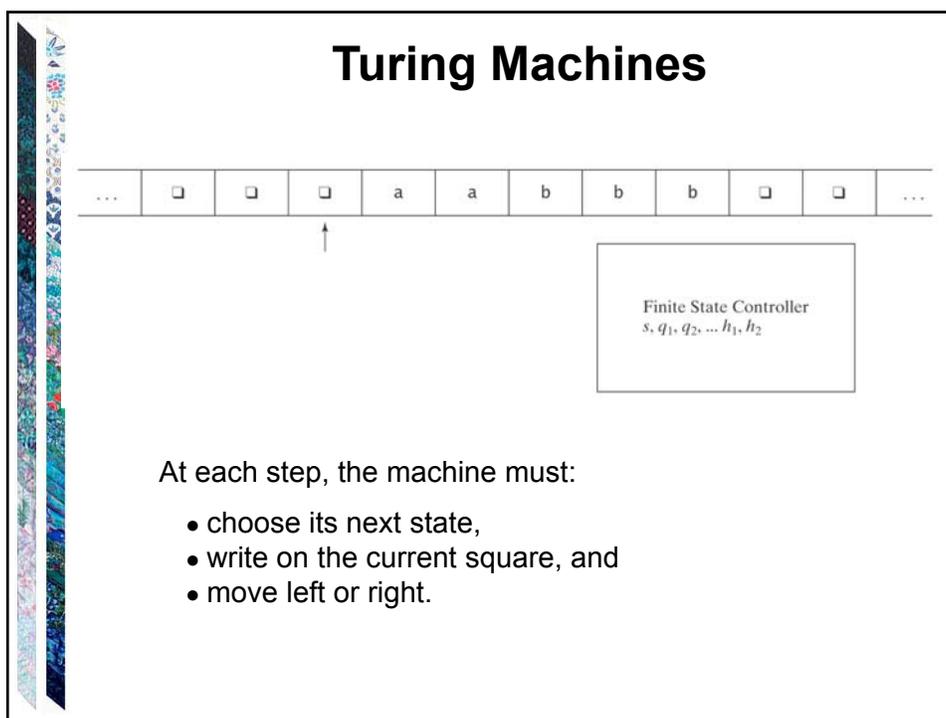
A (deterministic) Turing machine  $M$  is  $(K, \Sigma, \Gamma, \delta, s, H)$ , where

- $K$  is a finite set of states;
- $\Sigma$  is the input alphabet, which does not contain  $\epsilon$  ;
- $\Gamma$  is the tape alphabet,  
which must contain  $\epsilon$  and have  $\Sigma$  as a subset.
- $s \in K$  is the initial state;
- $H \subseteq K$  is the set of halting states;
- $\delta$  is the transition function:

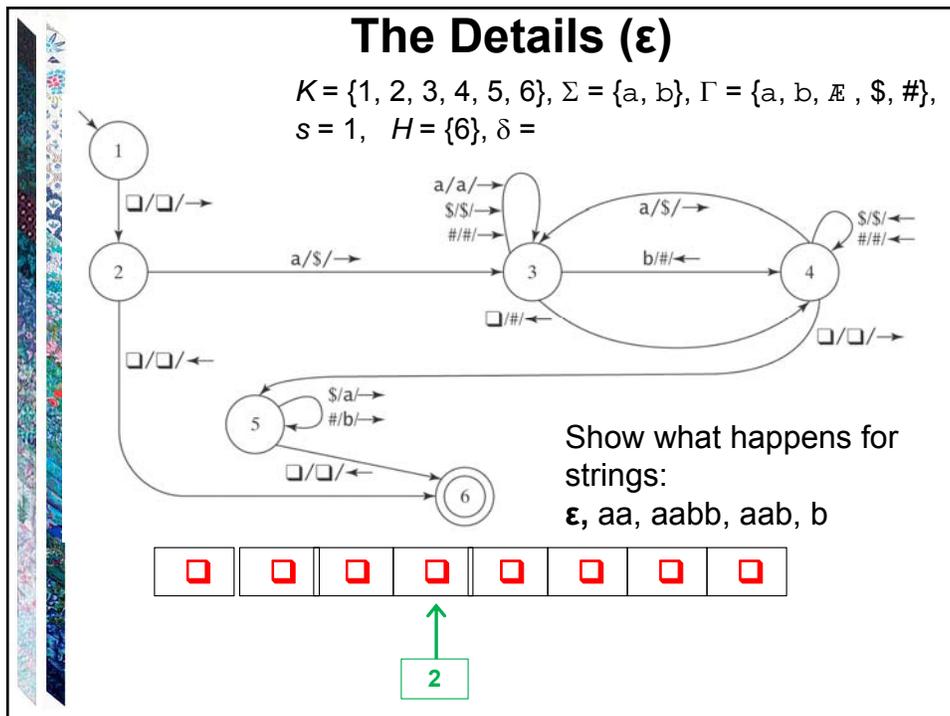
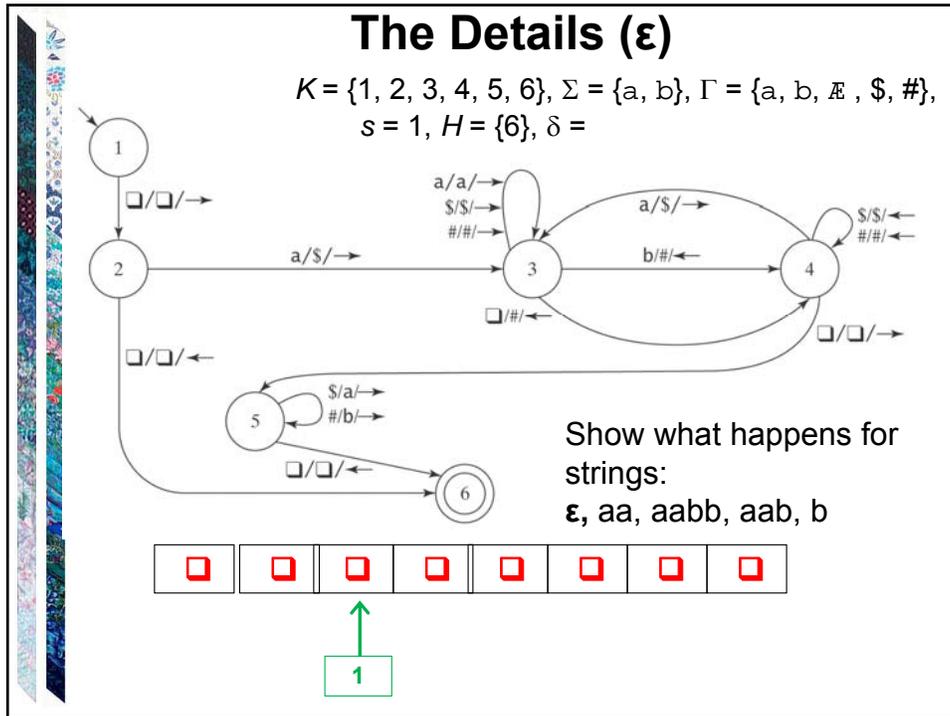
$$(K - H) \times \Gamma \quad \text{to} \quad K \times \Gamma \times \{\rightarrow, \leftarrow\}$$

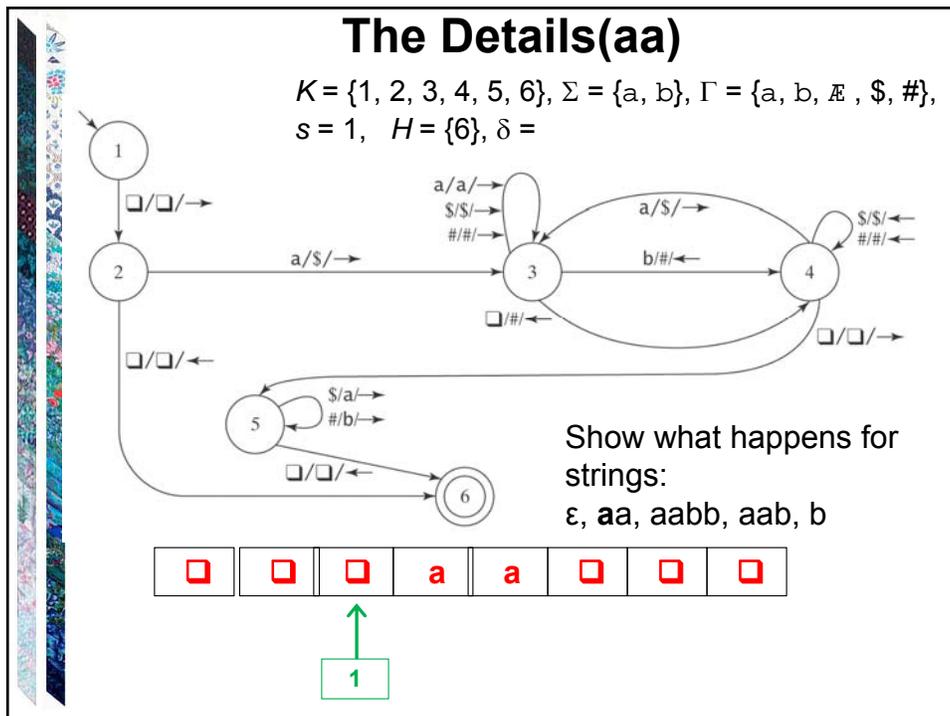
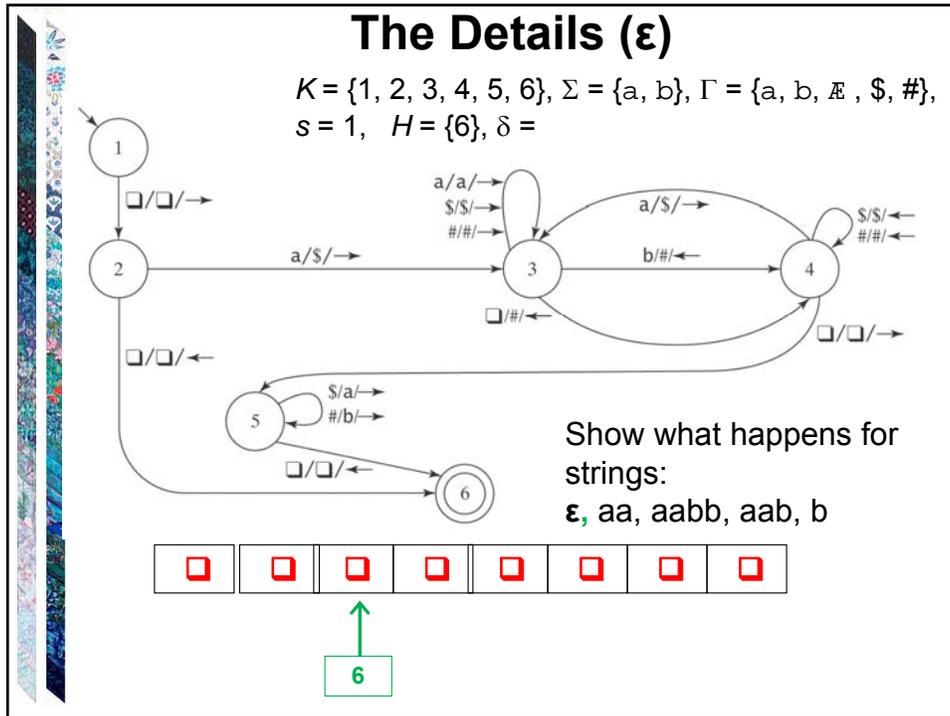
non-halting state  $\times$  tape char  $\rightarrow$  state  $\times$  tape char  $\times$  direction to move (R or L)

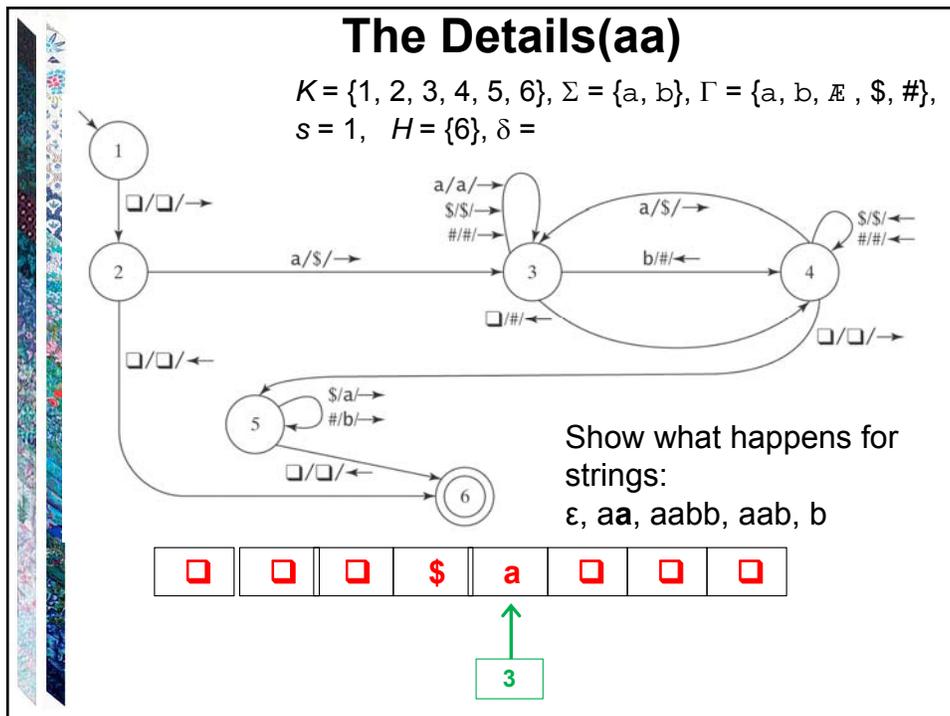
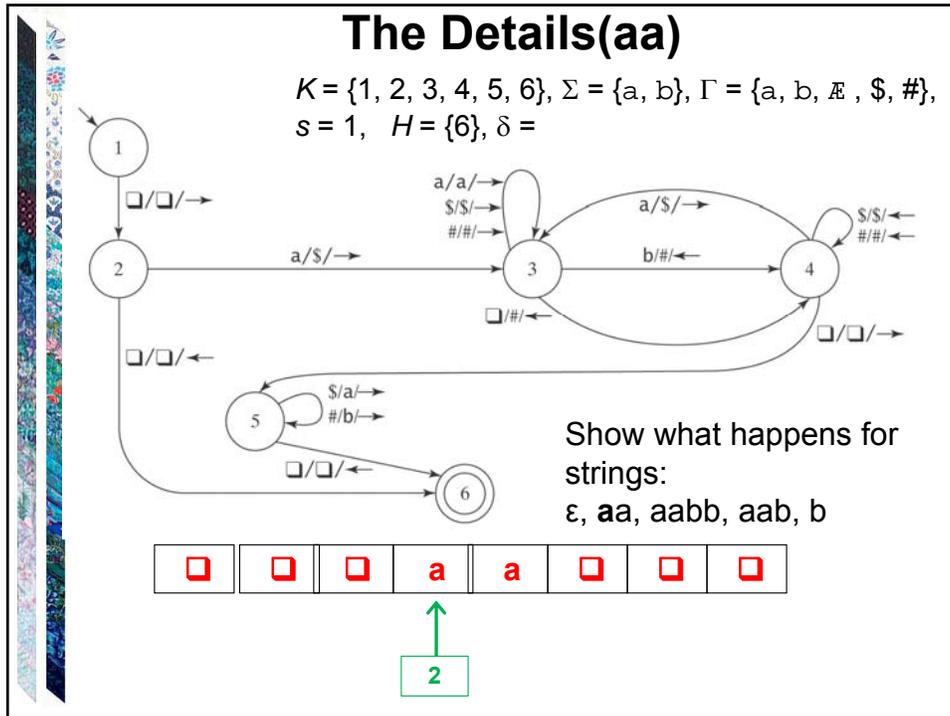
Finite State Controller  
 $s, q_1, q_2, \dots, h_1, h_2$

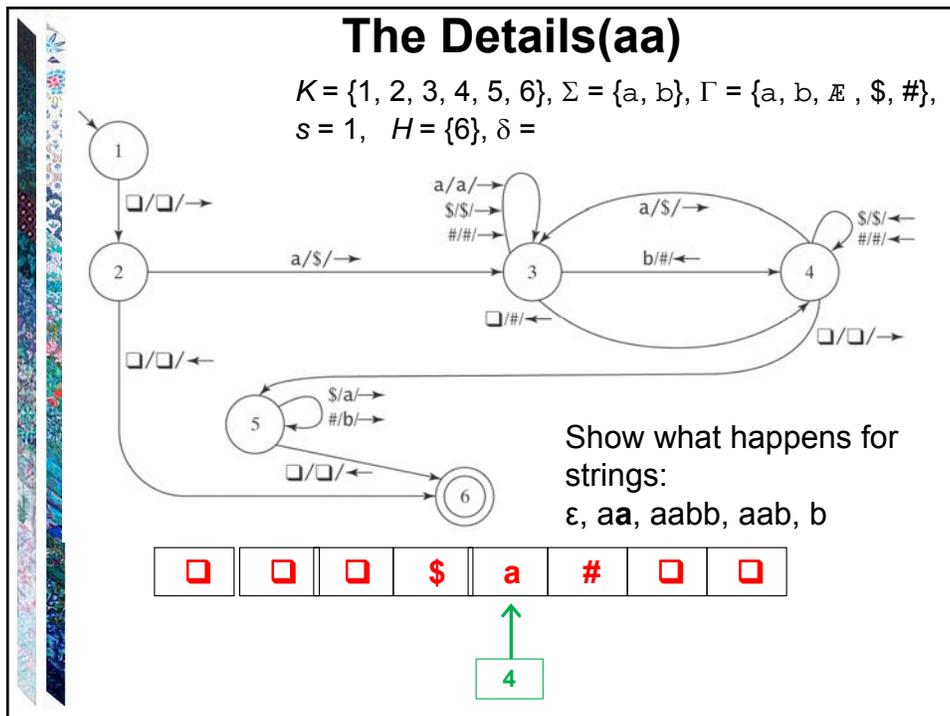
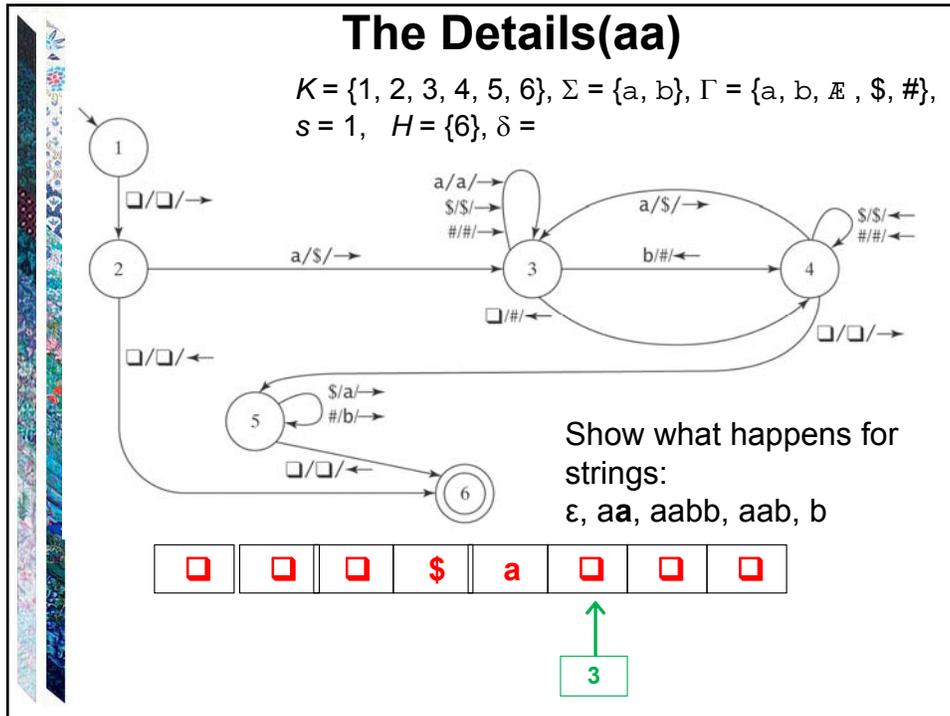


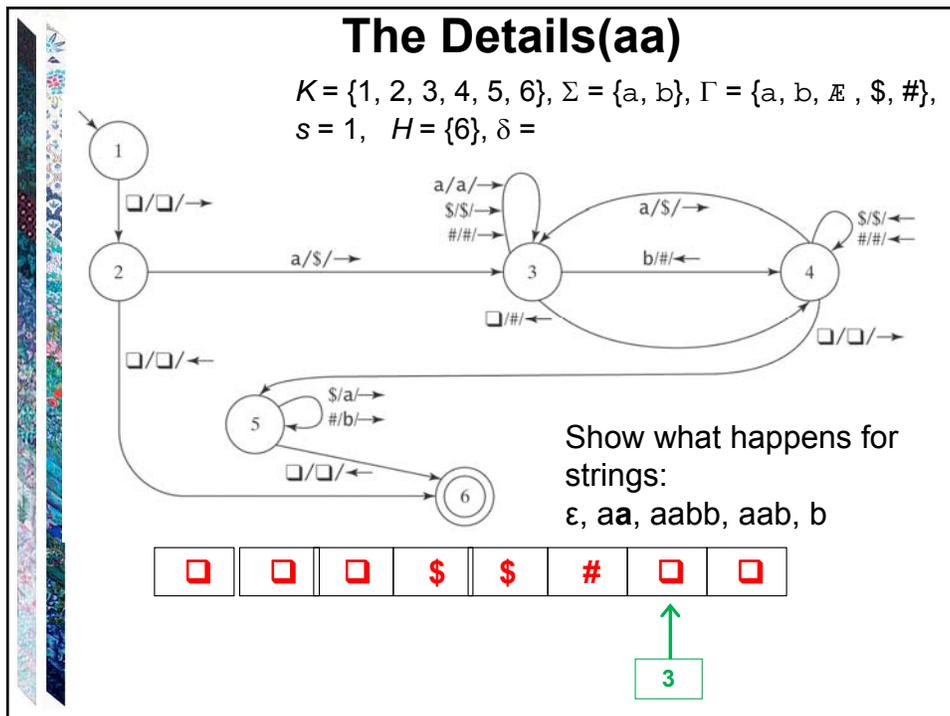
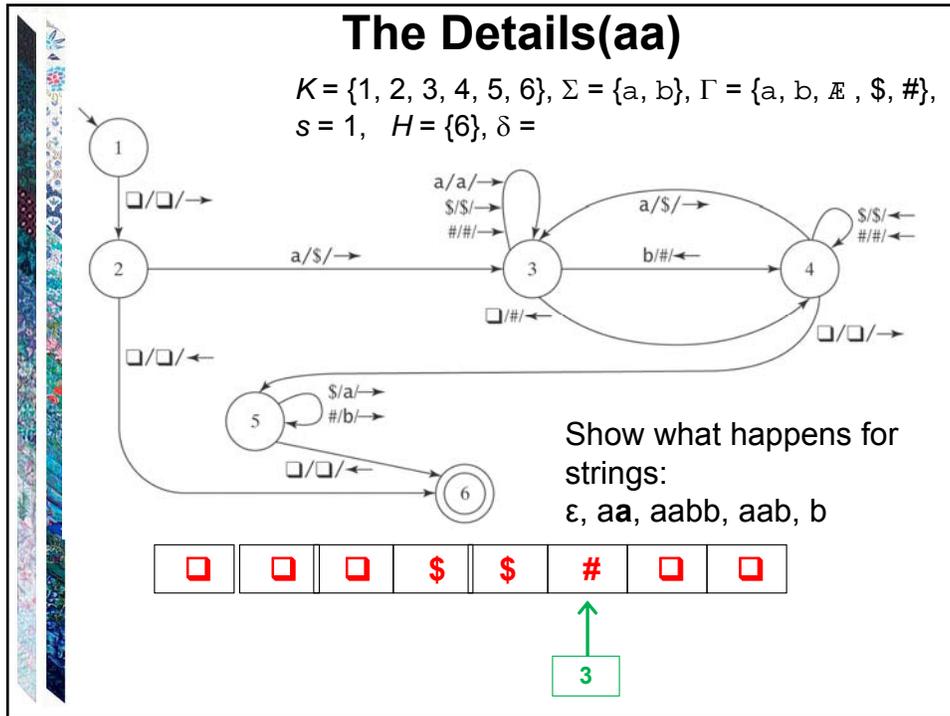


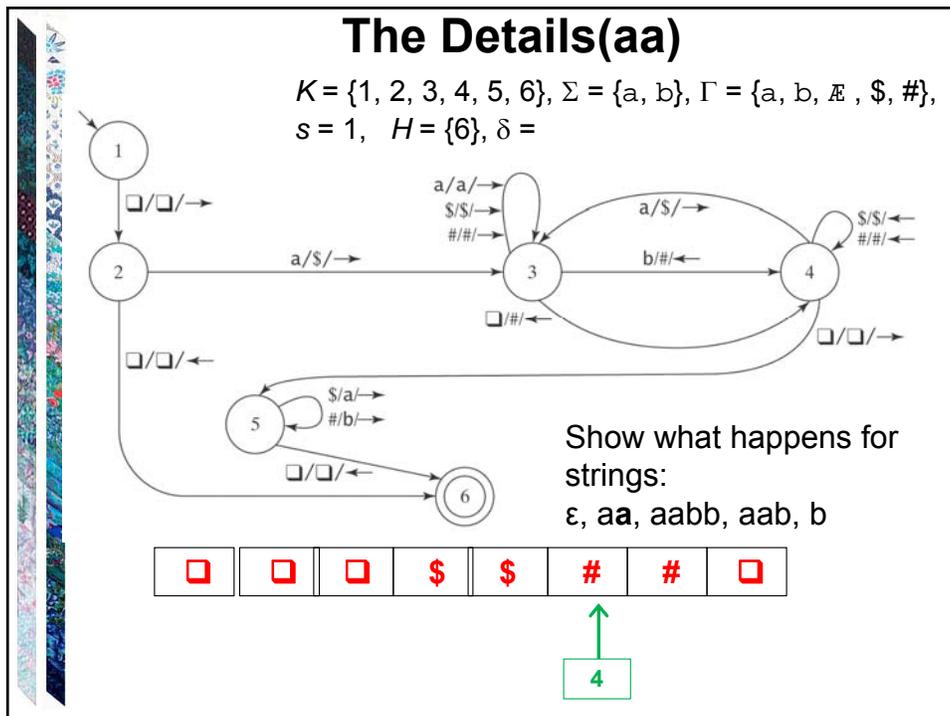
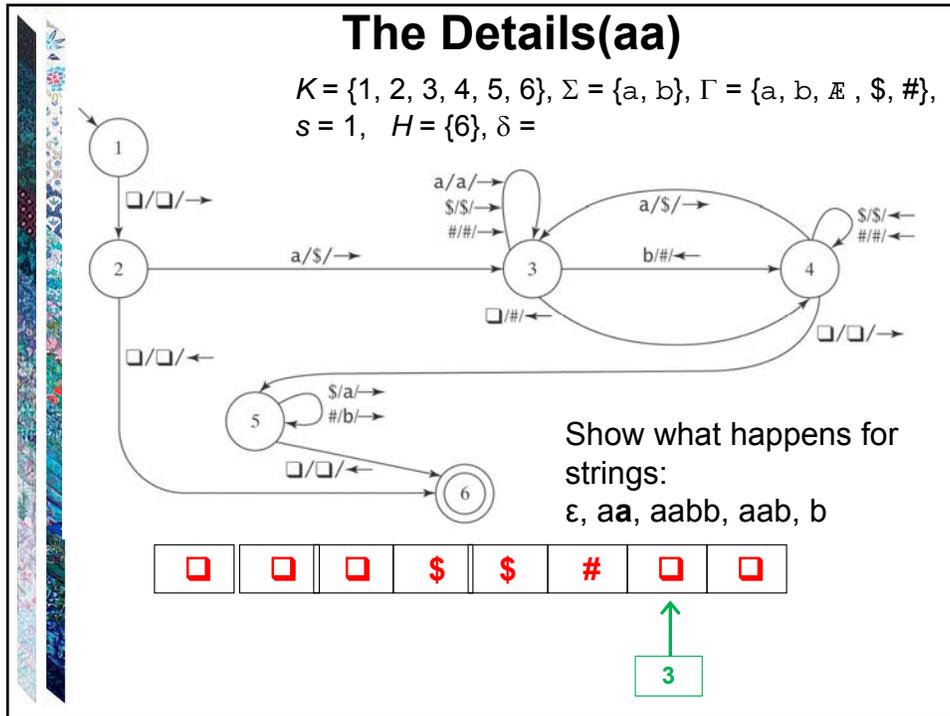


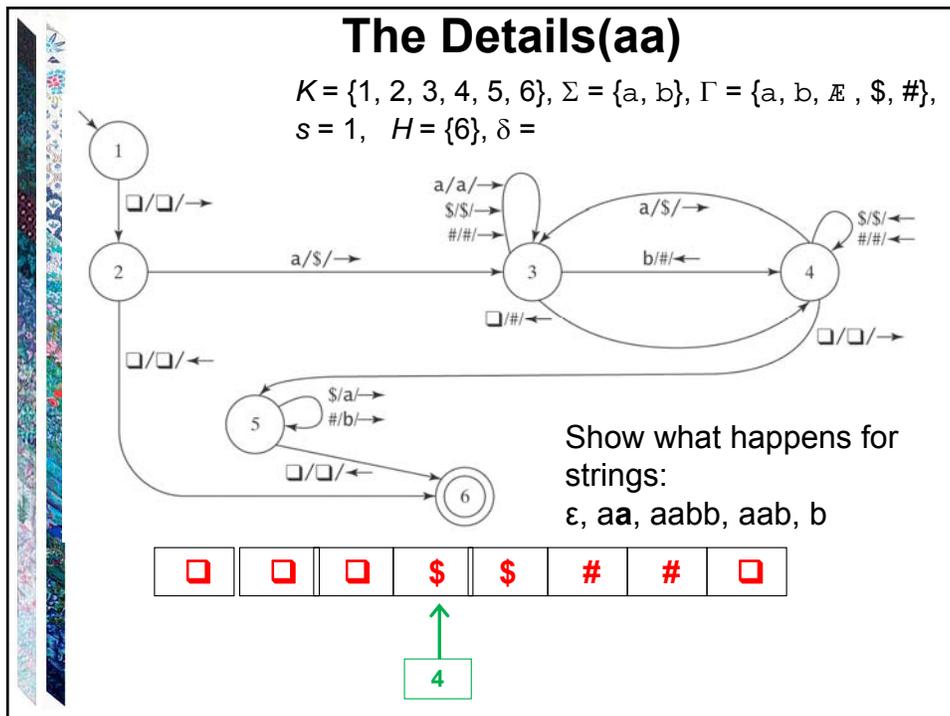
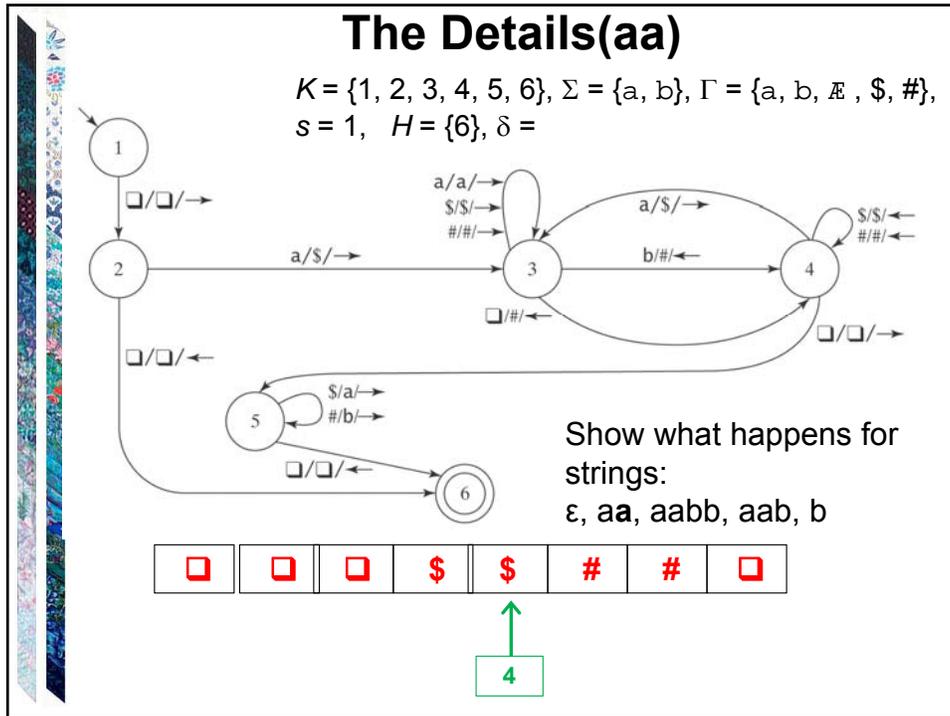


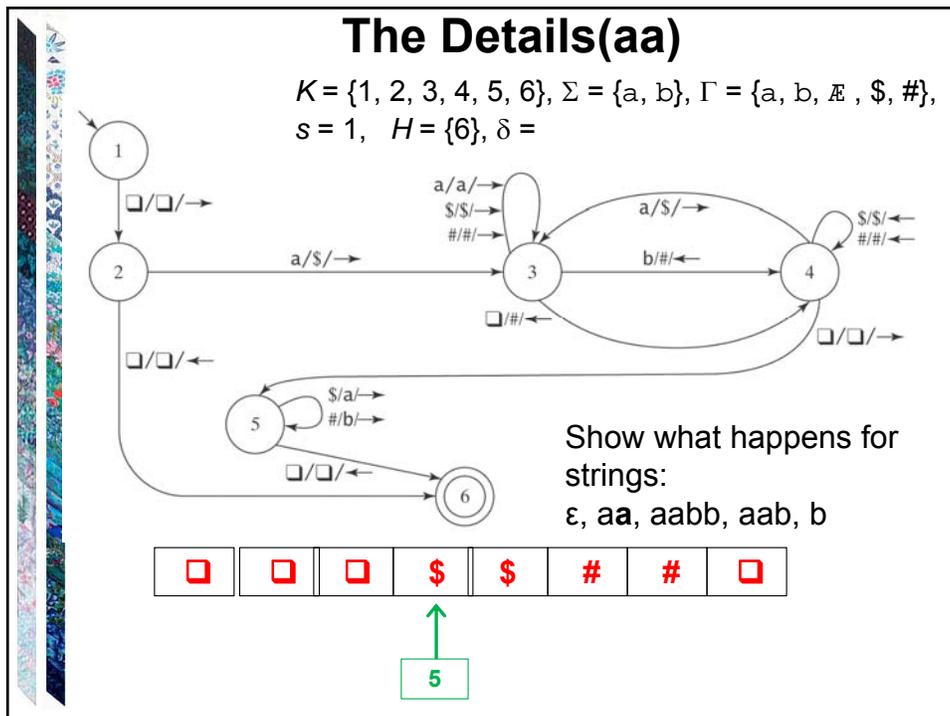
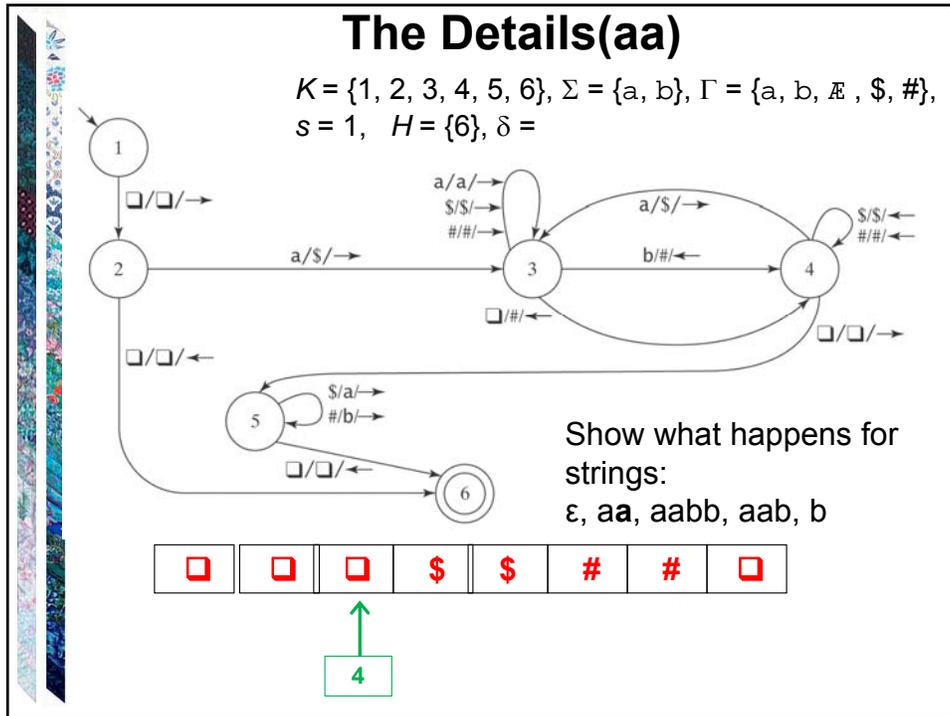


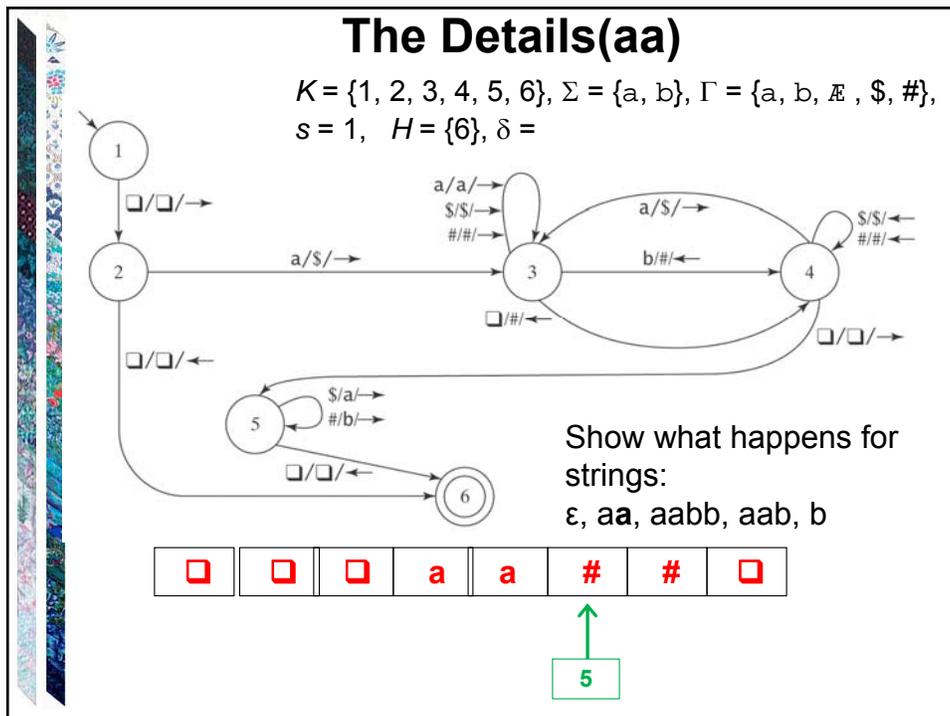
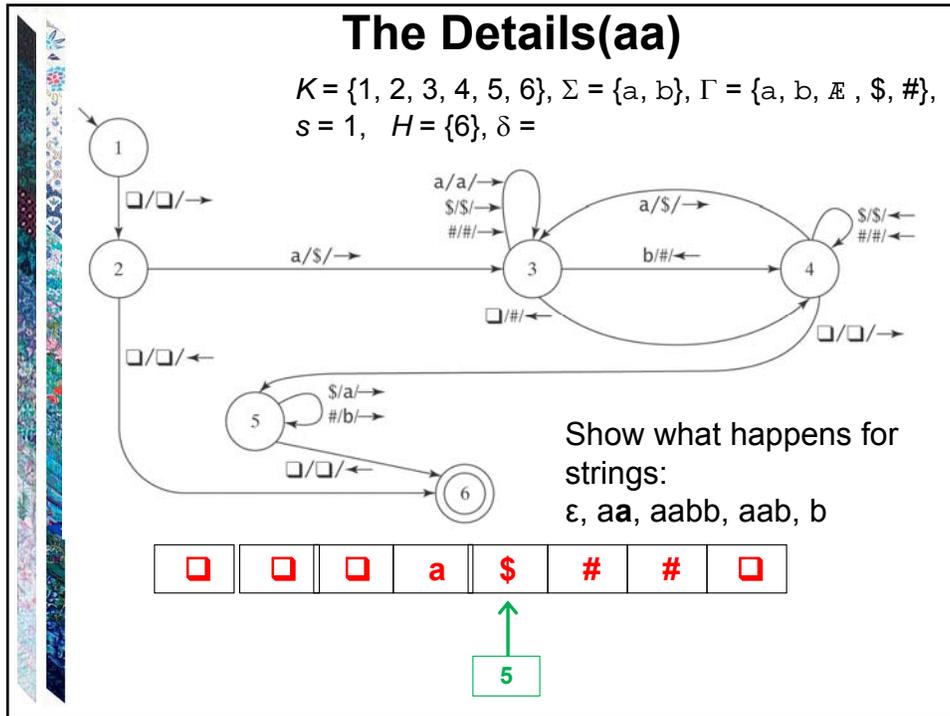


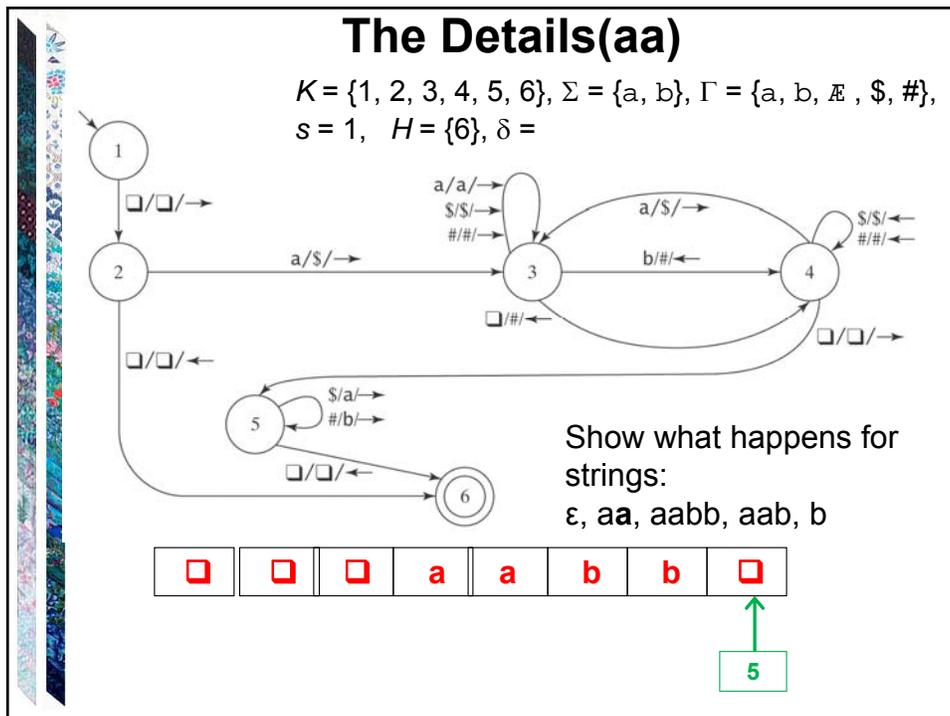
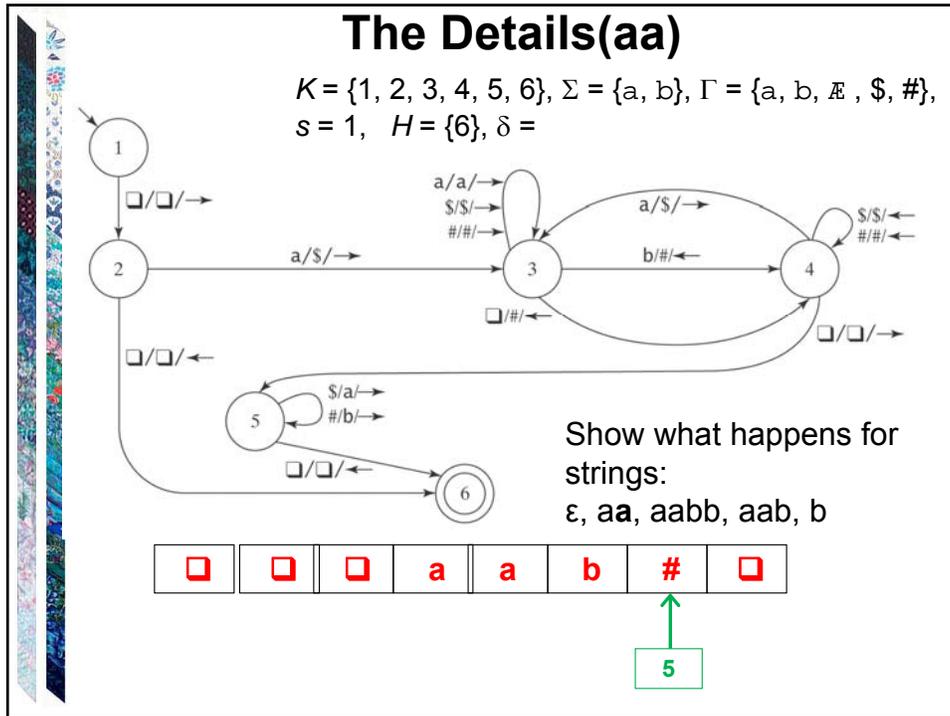


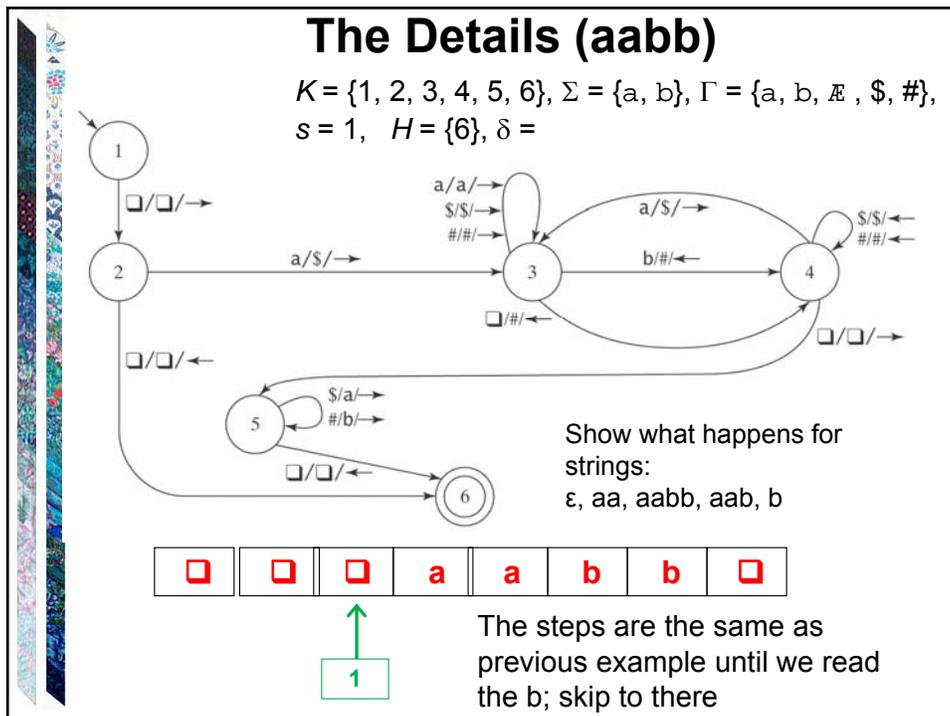
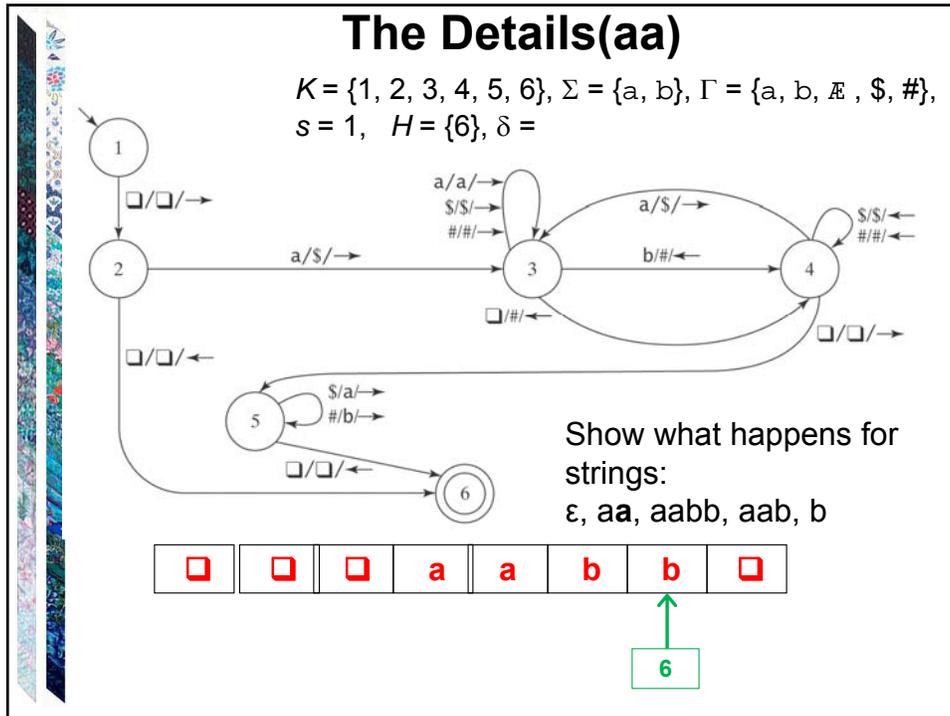


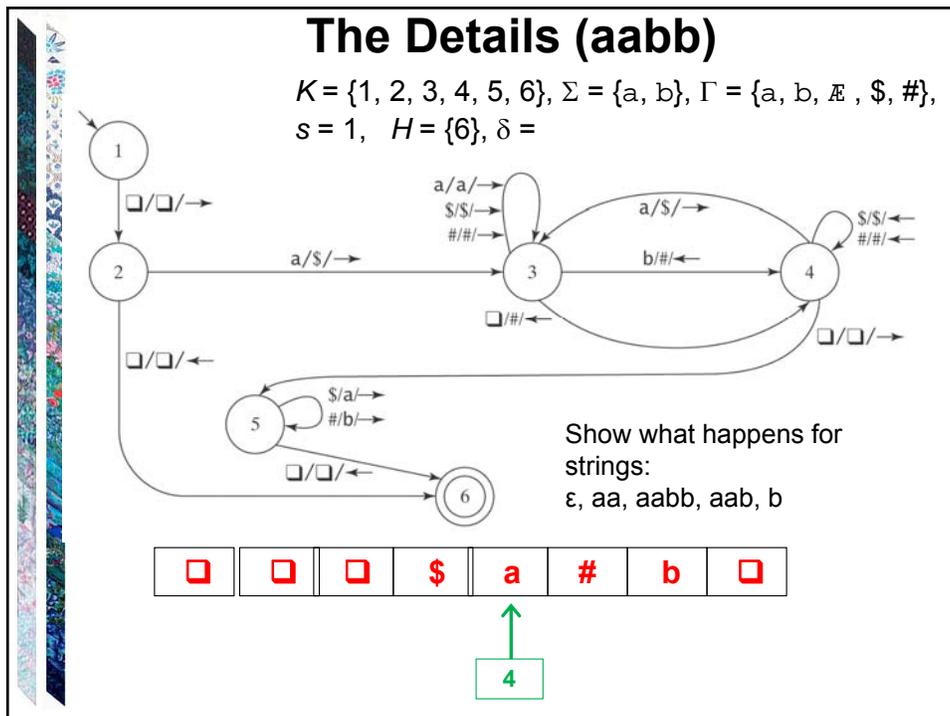
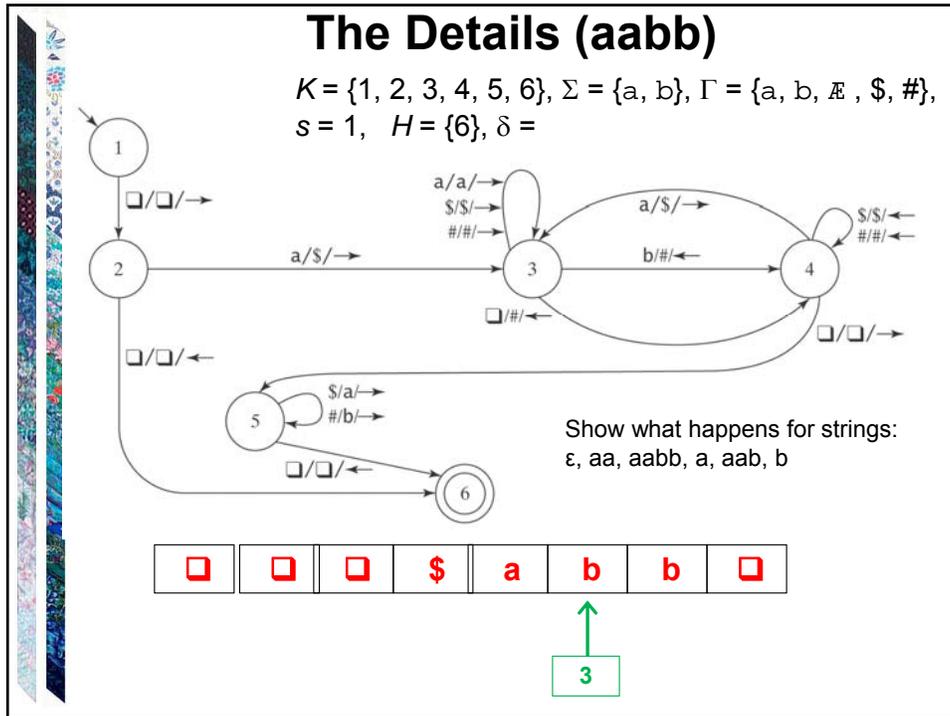


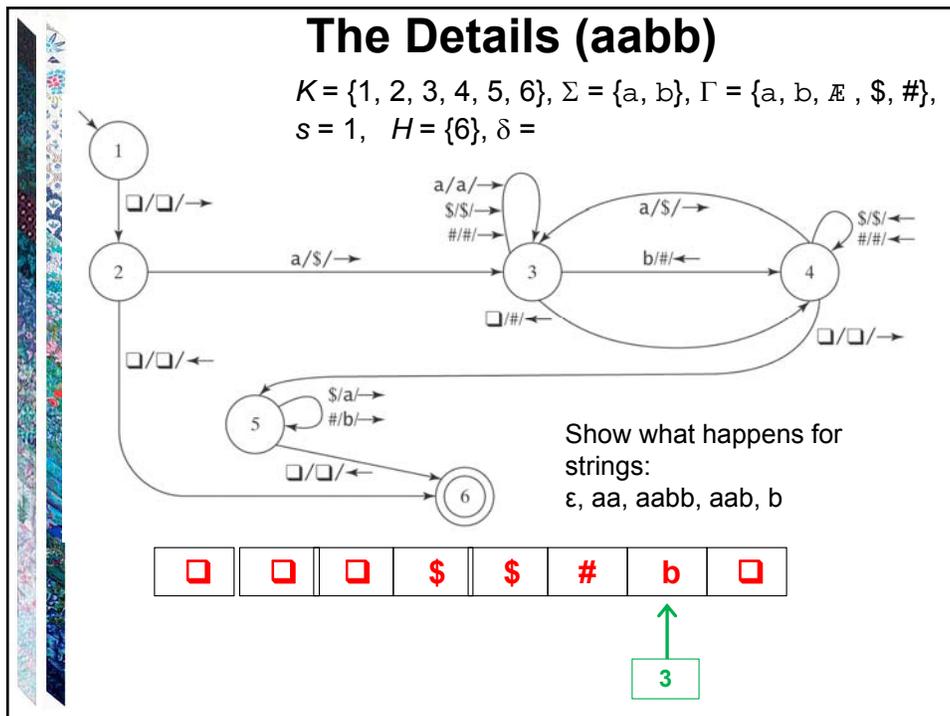
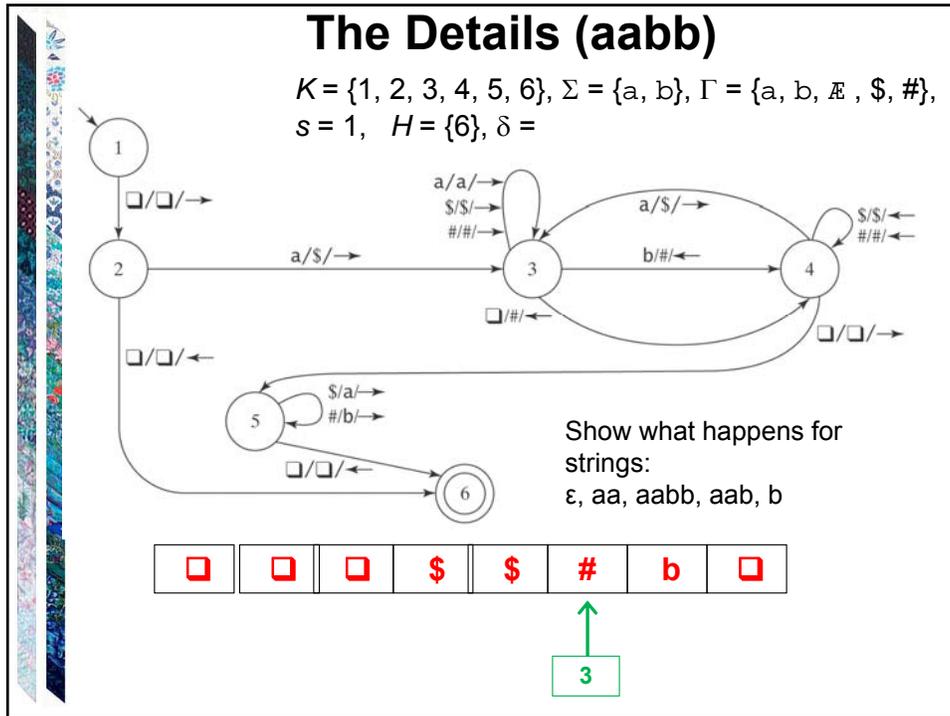


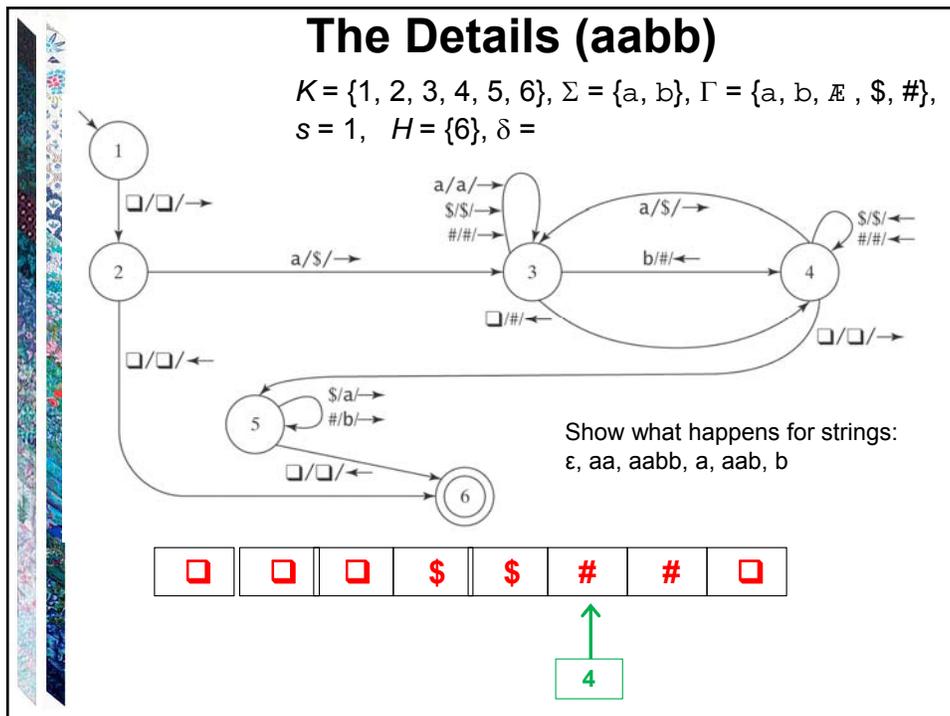
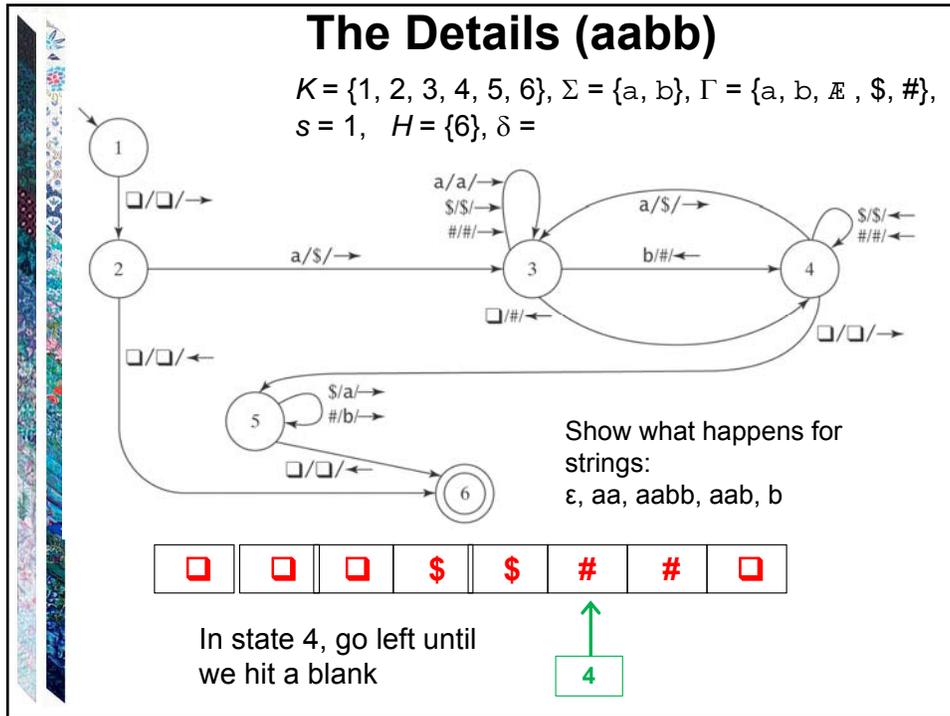


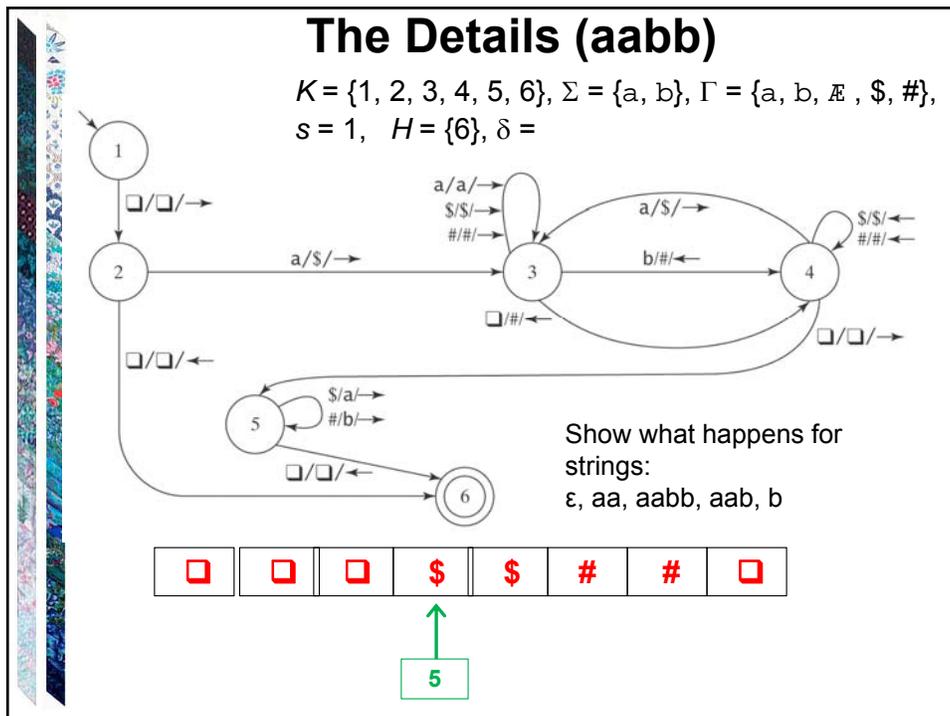
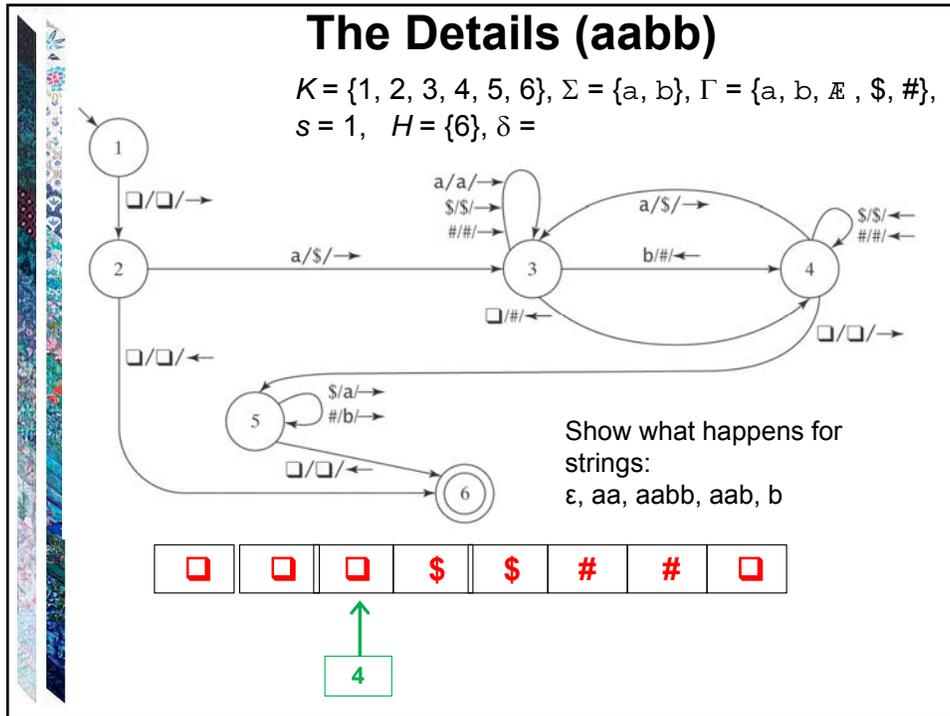


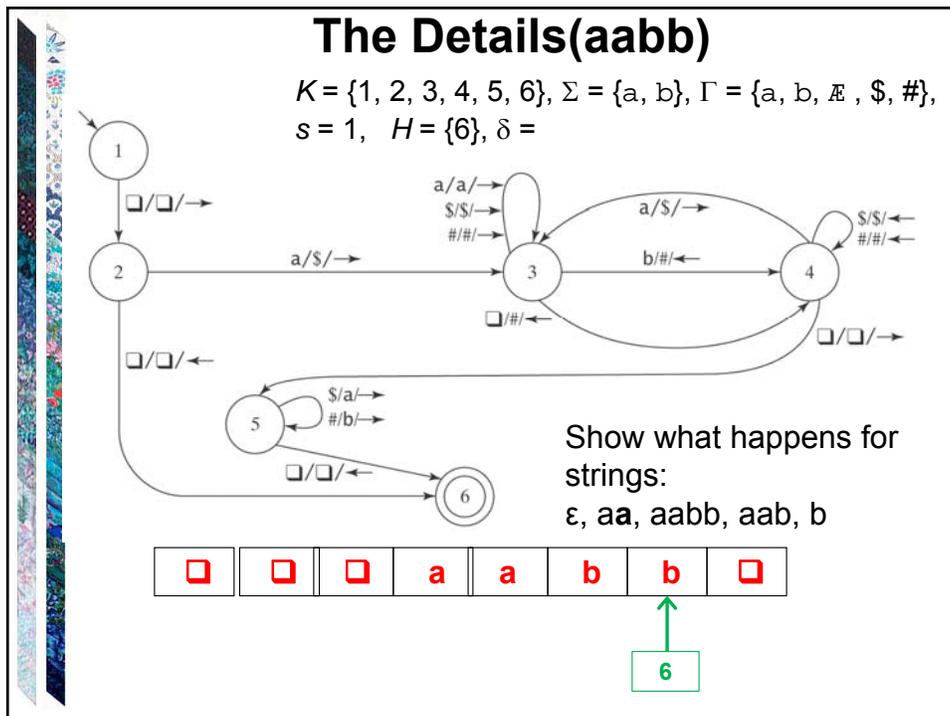
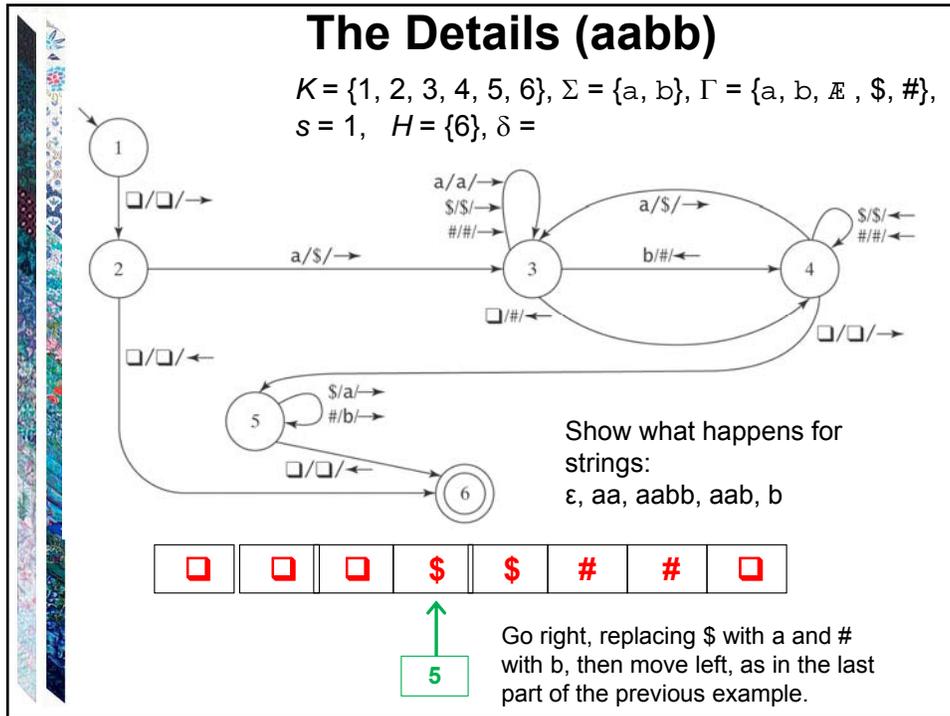


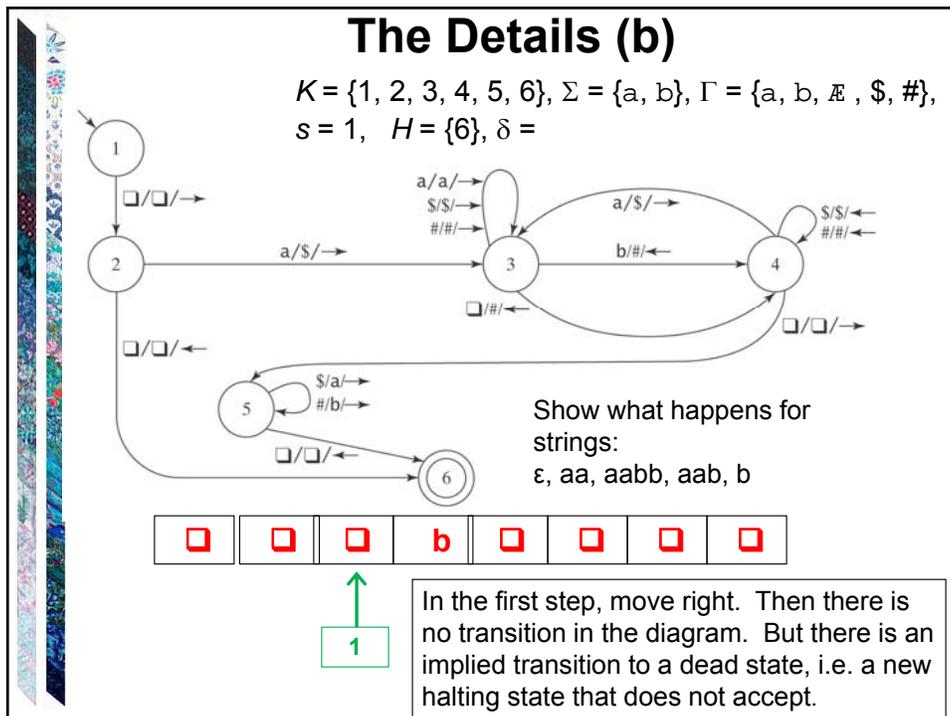
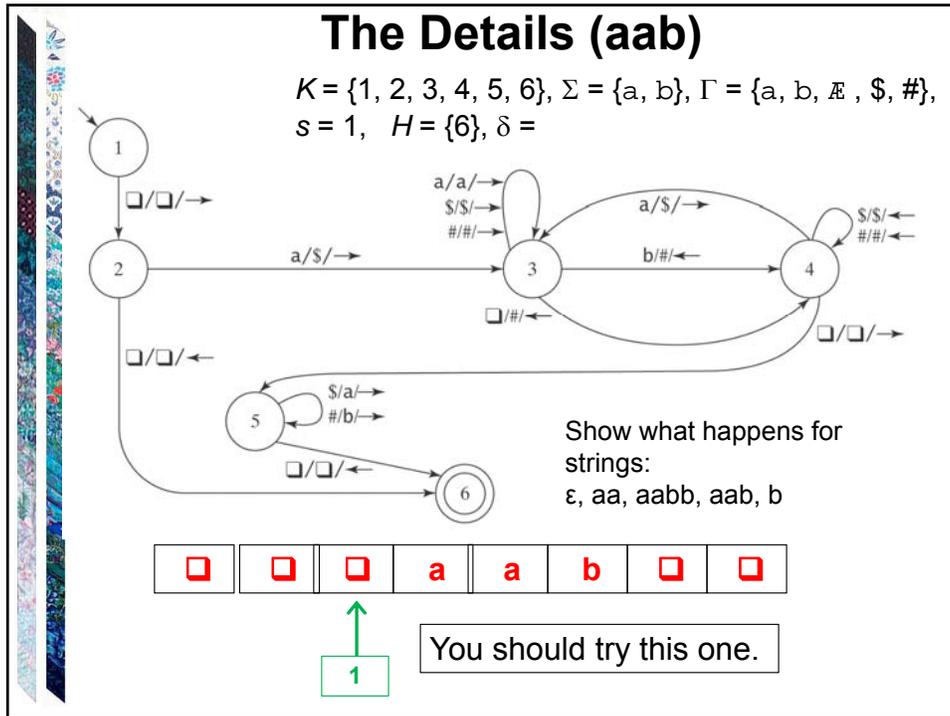












## Notes on Programming

Turing machines have a strong procedural feel, with one phase coming after another.

There are common idioms, such as "scan left until you find a blank"

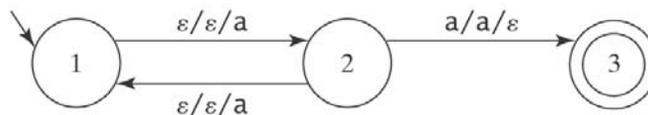
There are some common ways to scan back and forth marking things off.

Often there is a final phase to fix up the output.

Even a very simple machine can be a nuisance to write.

## Halting

- A **DFSM**  $M$ , on input  $w$ , is guaranteed to halt in  $|w|$  steps.
- A **PDA**  $M$ , on input  $w$ , is not guaranteed to halt. To see why, consider again  $M =$



But there exists an algorithm to construct an equivalent PDA  $M'$  that is guaranteed to halt.

- A **TM**  $M$ , on input  $w$ , is not guaranteed to halt. And there is no algorithm to construct an equivalent TM that is guaranteed to halt.

## Formalize TM computations

A configuration of TM  $M = (K, \Sigma, \Gamma, s, H)$  is an element of:

$$K \times ((\Gamma - \{\epsilon\}) \Gamma^*) \cup \{\epsilon\} \times \Gamma \times (\Gamma^* (\Gamma - \{\epsilon\})) \cup \{\epsilon\}$$

state	before current tape square	current tape square	after current tape square
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## Example Configurations



Second **a**  
should be **b**

- (1)  $(q, ab, b, b)$  written more simply as  $(q, ab\underline{bb})$
- (2)  $(q, \epsilon, \epsilon, aabb)$  written more simply as  $(q, \underline{\epsilon} abbb)$

Initial configuration is always  $(s, \underline{\epsilon} w)$

## Yields and Computations

$(q_1, w_1) \vdash_M (q_2, w_2)$  iff  $(q_2, w_2)$  is derivable *via*  $\delta$  in one step.

For any TM  $M$ , let  $\vdash_M^*$  be the reflexive, transitive closure of  $\vdash_M$ .

Configuration  $C_1$  **yields** configuration  $C_2$  if:  $C_1 \vdash_M^* C_2$ .

A **path** through  $M$  is a sequence of configurations  $C_0, C_1, \dots, C_n$  for some  $n \geq 0$  such that  $C_0$  is the initial configuration and:

$$C_0 \vdash_M C_1 \vdash_M C_2 \vdash_M \dots \vdash_M C_n.$$

A **computation** by  $M$  is a path that halts.

If a computation is of *length*  $n$  (has  $n$  steps), we can also write:

$$C_0 \vdash_M^n C_n$$

## Exercise

If  $n$  and  $m$  are non-negative integers, ***monus*** $(n, m)$  is defined to be  $n - m$  if  $n > m$ , and 0 if  $n \leq m$ .

Draw the diagram for a TM  $M$  whose input is  $1^n; 1^m$  and whose output is  $1^{\text{monus}(n, m)}$ .

When  $M$  halts, the read/write head should be positioned on the blank before the first 1.



## (not used in class 2018) Exercise

A TM to recognize  $\{ ww^R : w \in \{a, b\}^* \}$ .

If the input string is in the language, the machine should halt with  $y$  as its current tape symbol

If not, it should halt with  $n$  as its current tape symbol.

The final symbols on the rest of the tape may be anything.



## TMs are complicated

- ... and *very* low-level!
- We need higher-level "abbreviations".
  - Macros
  - Subroutines

## A Macro language for Turing Machines

(1) Define some basic machines

- Symbol writing machines

For each  $x \in \Gamma$ , define  $M_x$ , written as just  $x$ , to be a machine that writes  $x$ . Read-write head ends up in original position.

- Head-moving machines

R: for each  $x \in \Gamma$ ,  $\delta(s, x) = (h, x, \rightarrow)$

L: for each  $x \in \Gamma$ ,  $\delta(s, x) = (h, x, \leftarrow)$

You need to learn (but not memorize) this simple language. I will use it and I expect you to use it on HW and tests.

- Machines that simply halt:

$h$ , which simply halts (don't care whether it accepts).

$n$ , which halts and rejects.

$y$ , which halts and accepts.

## Checking Inputs and Combining Machines

Next we need to describe how to:

- Check the tape and branch based on what character we see, and
- Combine the basic machines to form larger ones.

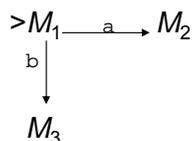
To do this, we need two forms:

- $M_1 M_2$

- $M_1 \xrightarrow{\langle \text{condition} \rangle} M_2$

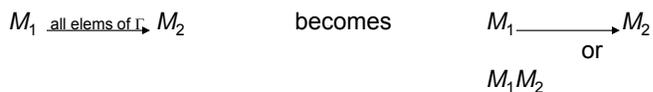
## Turing Machines Macros Cont'd

Example:

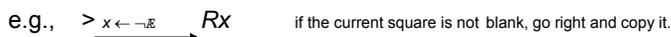
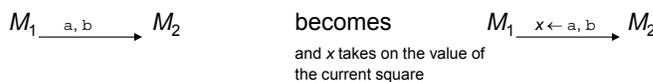
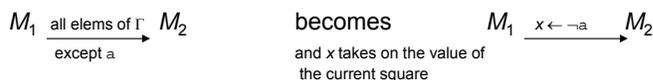


- Start in the start state of  $M_1$ .
- Compute until  $M_1$  reaches a halt state.
- Examine the tape and take the appropriate transition.
- Start in the start state of the next machine, etc.
- Halt if any component reaches a halt state and has no place to go.
- If any component fails to halt, then the entire machine may fail to halt.

## More macros



### Variables



## Blank/Non-blank Search Machines

	Find the first blank square to the right of the current square.	$R_{\epsilon}$
	Find the first blank square to the left of the current square.	$L_{\epsilon}$
	Find the first nonblank square to the right of the current square.	$R_{\neg\epsilon}$
	Find the first nonblank square to the left of the current square.	$L_{\neg\epsilon}$

## More Search Machines

$L_a$	Find the first occurrence of $a$ to the left of the current square.
$R_{a,b}$	Find the first occurrence of $a$ or $b$ to the right of the current square.
<div style="border: 1px solid black; padding: 5px; display: inline-block;"> <math display="block">  \begin{array}{l}  L_{a,b} \xrightarrow{a} M_1 \\  \downarrow b \\  M_2  \end{array}  </math> </div>	Find the first occurrence of $a$ or $b$ to the left of the current square, then go to $M_1$ if the detected character is $a$ ; go to $M_2$ if the detected character is $b$ .
$L_{x \leftarrow a,b}$	Find the first occurrence of $a$ or $b$ to the left of the current square and set $x$ to the value found.
$L_{x \leftarrow a,b} R_x$	Find the first occurrence of $a$ or $b$ to the left of the current square, set $x$ to the value found, move one square to the right, and write $x$ ( $a$ or $b$ ).