# **Interaction Diagramming**

CSSE 374: Session 10

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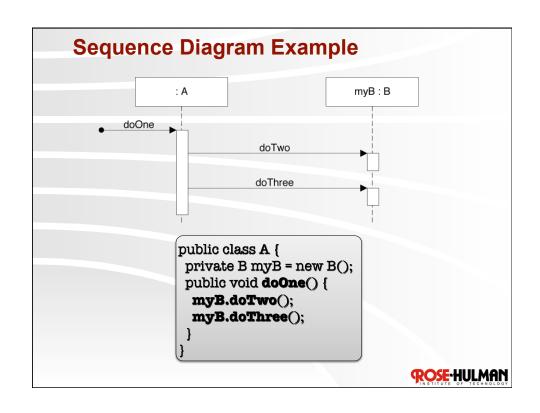
## **Interaction Diagrams**

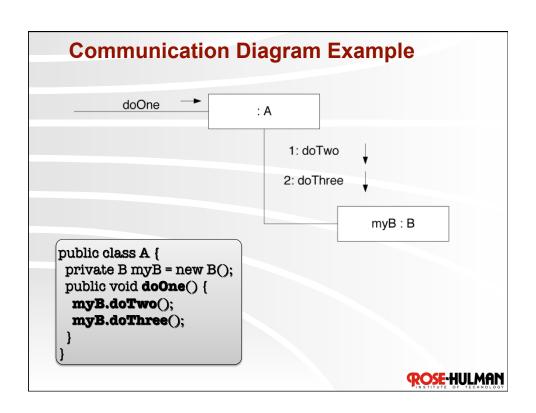
- Used for dynamic object modeling
  - Answer questions about behavior (i.e., events, sequencing)
- Two common types:
  - Sequence diagrams \*
  - Communication diagrams

Don't confuse with System Sequence Diagrams (SSDs), which use a subset of the notation

Q1,2







## **Relative Strengths**

- Sequence Diagrams (SD)
  - Clearer notation and semantics
  - Better tool support
  - Easier to follow
  - Excellent for documents
- Communication Diagrams (CD)
  - Much more space efficient
  - Easier to modify quickly
  - Excellent for UML as sketch

**ROSE-HULMAN** 

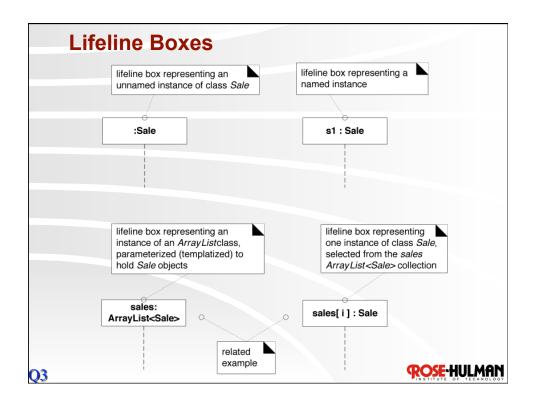
# **Why Bother with Interaction Diagrams?**

- Keep us from getting bogged down in syntax
- Can allocate responsibilities with minimal commitment

Did I say, "don't get bogged down"?

**ROSE-HULMAN** 





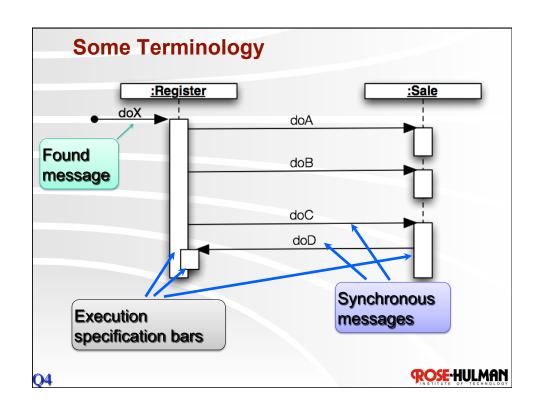
# Basic Message Expression Syntax id = message(parameter: parameterType): returnType \* Much is optional, for example: • initialize(register)

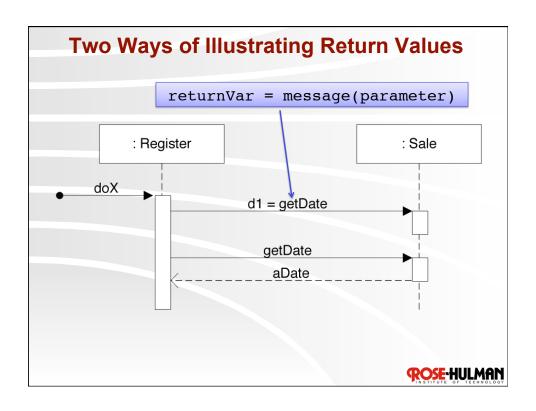
- initialize
- d = getProductDescription(id)
- d = getProductDescription(id:ItemID)
- d = getProductDescription(id:ItemID) : ProductDesc

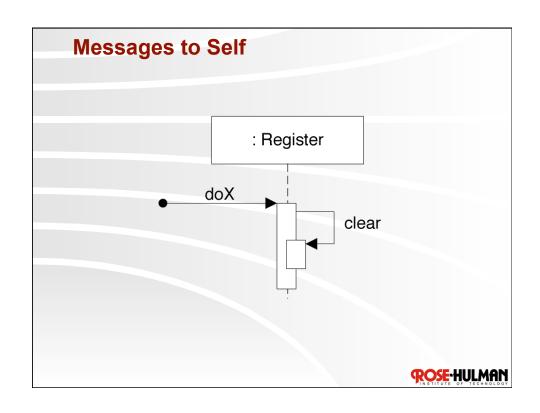


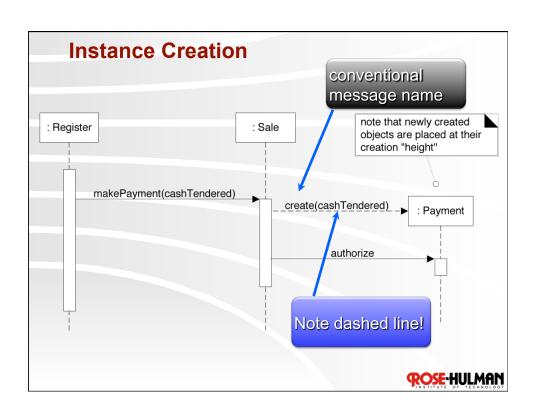
# **Sequence Diagrams**

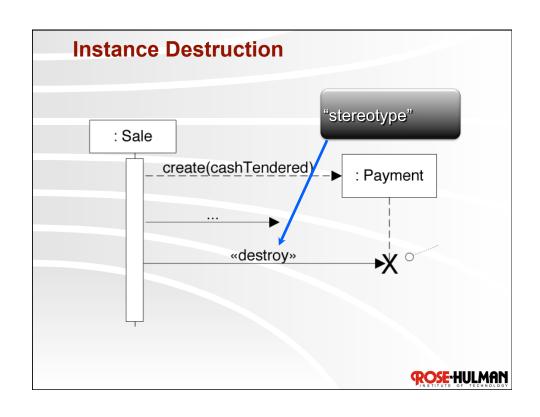


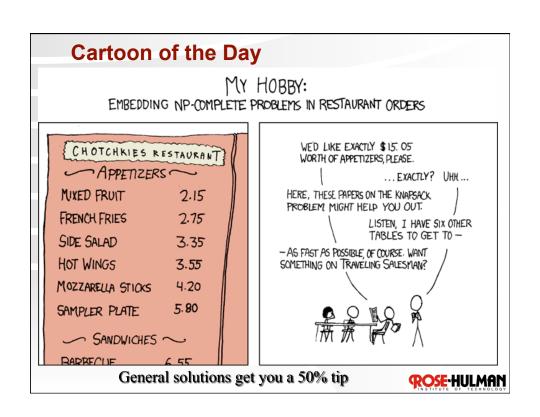


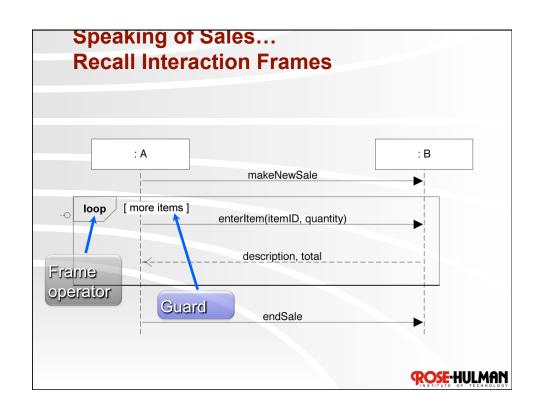




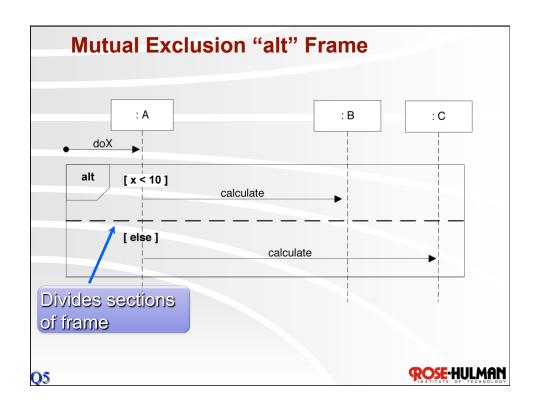


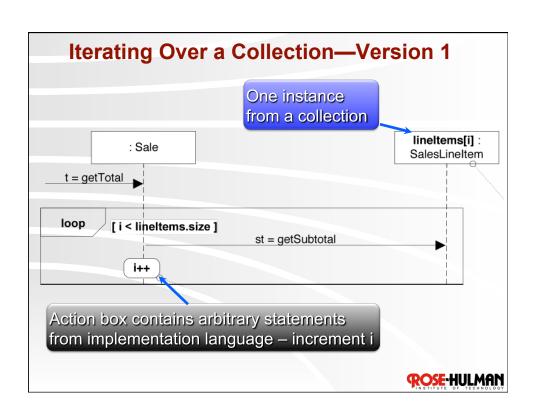


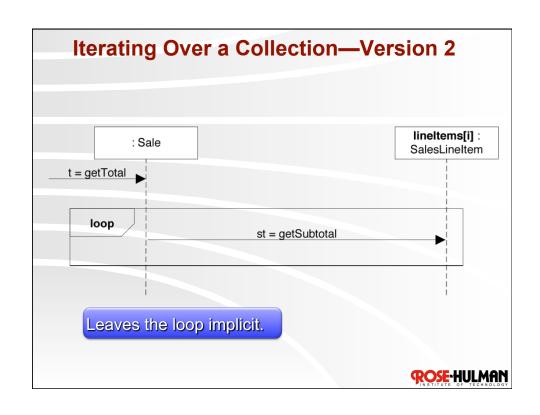


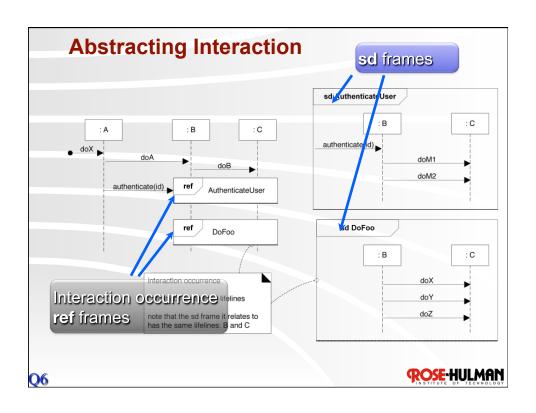


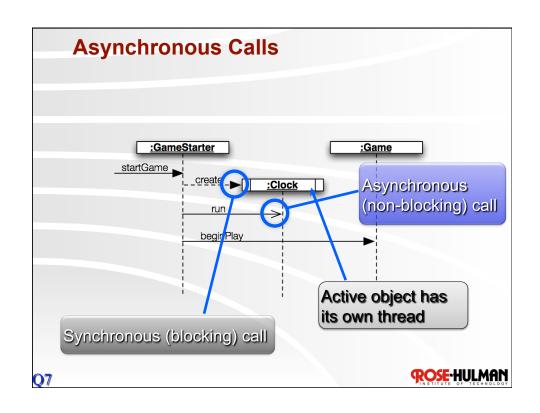
Operator	Meaning
alt	"alternative", if-then-else or switch
loop	loop while guard is true, or loop(n) times
opt	optional fragment executes if guard is true
par	parallel fragments
region	critical region (single threaded)
ref	a "call" to another sequence diagram
sd	a sequence diagram that can be "called"



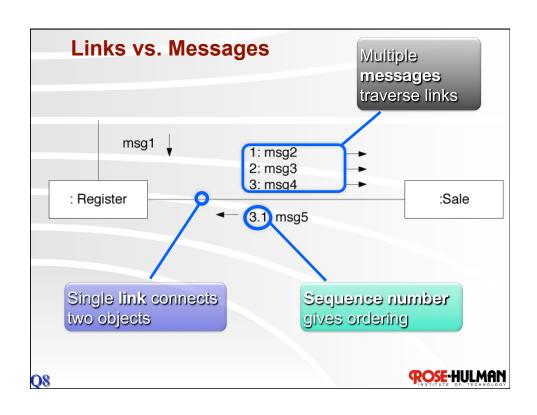


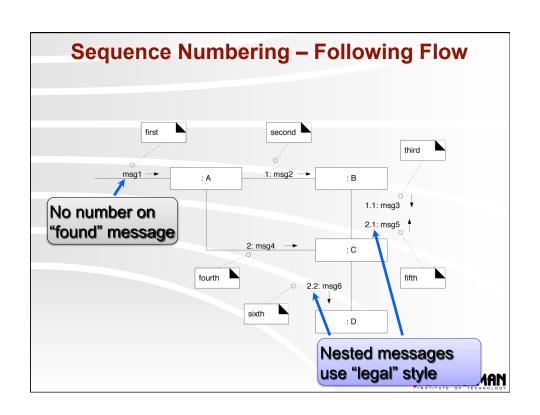


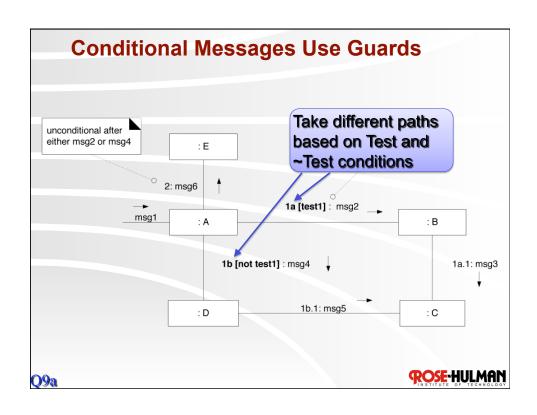


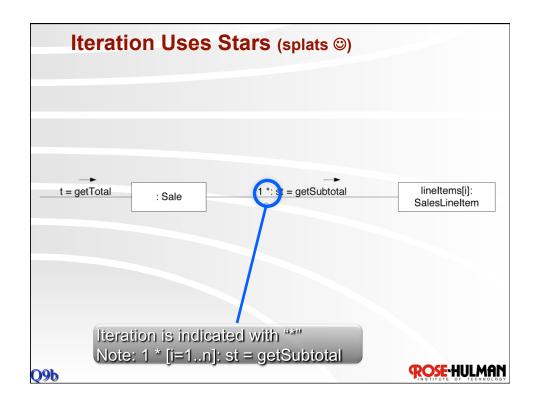


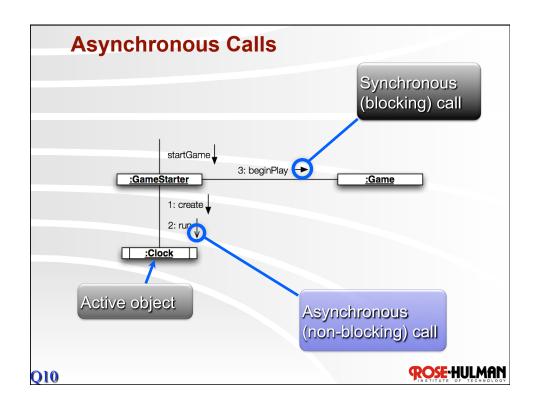












#### **Homework and Milestone Reminders**

- Read Chapter 16 on Design Class Diagrams
- Homework 3 Dog-eDoctor System SSDs and Operations Contracts
  - Due by 5:00pm on Today, December 15th, 2009
- Homework 4 Dog-eDoctor System
   Preliminary Logical Architecture and Design
  - Due by 5:00pm on Tuesday, January 5th, 2010
  - Extra credit if you get it in by 5:00pm this Friday!
- Milestone 3 Iteration 1: Junior Project
  - Finish Analysis Model (SSDs, OCs)
  - Logical Architecture Package Diagrams, and
  - 1st (initial) Version of System
  - Due by 11:59pm on Friday, January 8th, 2009

