CSSE 232 – Computer Architecture I
Rose-Hulman Institute of Technology
Computer Science and Software Engineering Department

Lab 7 – Instructor Verification

For each verification, be prepared to explain your answer and respond to other related questions that the lab TA’s or professors might ask. Turn this page in at the end of your lab period.

Team:__________________________ Date of Lab:_________

Demonstrate your memory and control unit.

Verified:__________________________ Date/Time:_________

23 November 2018