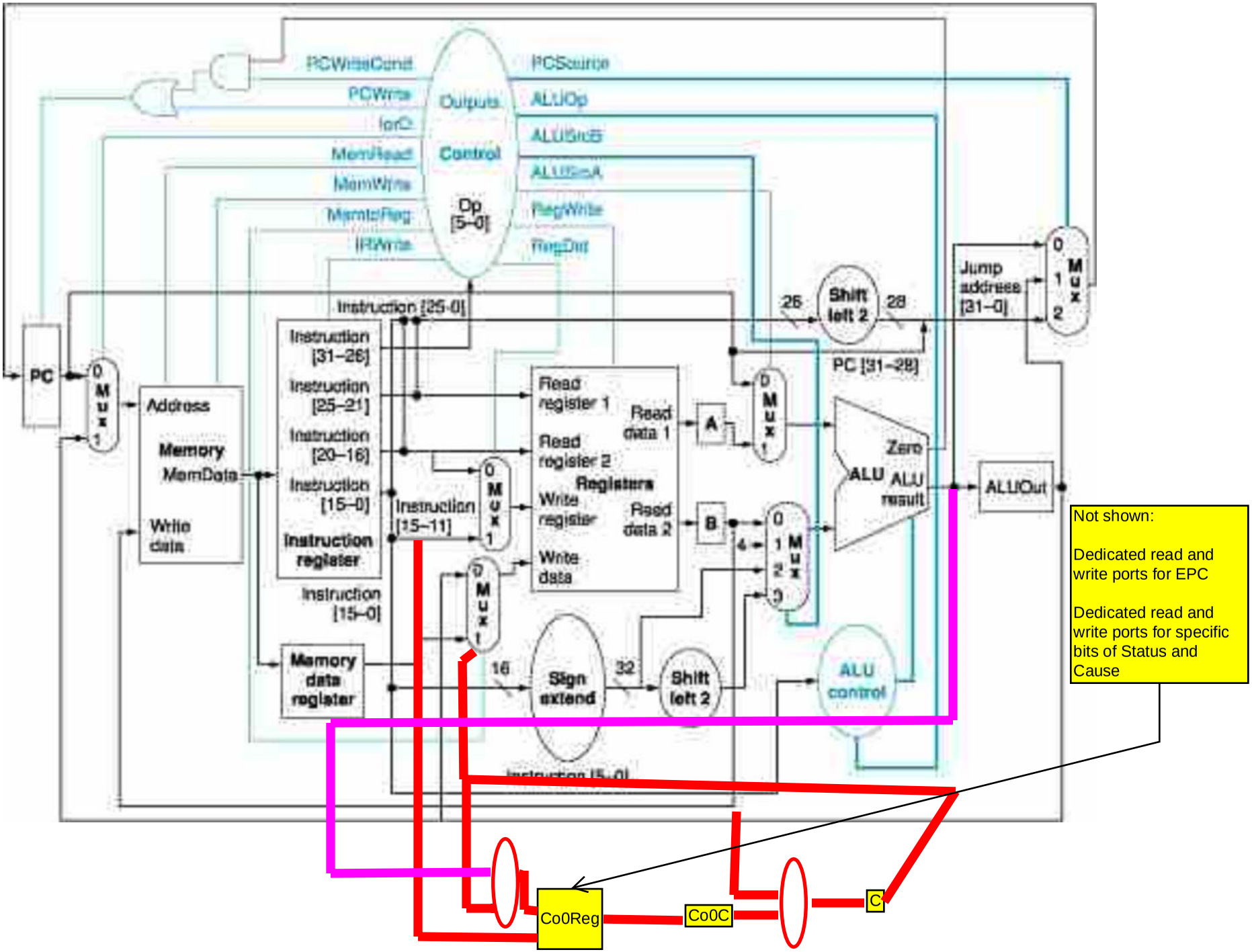
PCWriteCond
PCWrite
IorD
MemRead
MemWrite
MemtoReg
IRWrite
Outputs
Control
Op [5–0]
PCSource
ALUOp
ALUSrcB
ALUSrcA
RegWrite
RegDst
PC
Address
Memory
MemData
Write data
Instruction [31–26]
Instruction [25–21]
Instruction [20–16]
Instruction [15–0]
Instruction register
Instruction [25–0]
Instruction [15–11]
Instruction [15–0]
Memory data register
Read register 1
Read register 2
Registers
Write register
Write data
Read data 1
Read data 2
A
B
4
Sign extend
16
32
Shift left 2
26
28
PC [31–28]
Jump address [31–0]
ALU
Zero
ALU result
ALUOut
ALU control
Instruction [5–0]
Not shown:
Dedicated read and write ports for EPC
Dedicated read and write ports for specific bits of Status and Cause
Co0Reg
Co0C
C