CSSE 232 – Computer Architecture I
Rose-Hulman Institute of Technology
Computer Science and Software Engineering Department

Schedule

Day	Date	Reading	Topic	Problems
1	02 Sep	Ch 1	Introduction, Project overview, Performance lite	1.51,52
2		2.1–4; A.9,10; 2.5	Introduction to MIPS, SPIM, Logical instructions	2.6,7,59
3		2.6	Decision Instructions	2.10,11,14
4	09 Sep	2.9,7; A.6	MIPS addressing	2.5,33,36,39
5		2.10–13; A.1–5	Procedures, Putting it together	2.16–18
6		2.15–18; 2.8; A.8	Other architectures, I/O	2.23,52,54
7	16 Sep	3.1–3; A.7	Arithmetic for computers, Exceptions	3.1,2,5,6,9,10,30
8		App B	Constructing an ALU, Xilinx	3.24,25
9			Xilinx, More ALU	3.18,20
10	23 Sep		Project time, Exam I (evening)	
11			Recursion, Stacks	
12		5.1–3	Building a datapath	**M1**
13	30 Sep	5.4	Singlecycle datapath	5.2,13
14		5.5	Singlecycle datapath, Multicycle datapath	
15			Project time	
16	07 Oct		Multicycle datapath	5.32,35,36,37,44
17		5.6; A.7	Exceptions	5.52
18			Project time	**M2**
19	18 Oct	App C	Control (FSM)	
20		5.7,8	Control (Microcode), Verilog	5.53,54
21		Ch 4	Performance, Xilinx	**M3** 4.8,910
22	25 Oct	3.4	Multiplication	3.27(c),28(c),23
23		3.5	Division	3.27(d),28(d),29
24			Project time	**M4**
25	01 Nov	3.6	Floating point	3.30(c),31(c),48
26			Project time	
27			Exam II (evening)	**M5**
28	08 Nov		Issues, Wrap-up	
29			Project time	
30			Project time	