Rose-Hulman Institute of Technology
Computer Science and Software Engineering
Prof. Archana Chidanandan

Term Project Milestone 2 Evaluation Team #_2-3___ Points: 37/ 40

Evaluation Criteria Categories	Specific Criteria	Comments	Score
Consistency with higher level specifications	❑ Every instruction allowed by the machine language (ML) specification has a unique register transfer language (RTL) representation ❑ The sequences of register transfers specified by each RTL description correctly implement the functions described in the assembly language (AL) specification ❑ Every component referenced in the RTL descriptions is determined ❑ For each component, input, output, and control signals that are sufficient to implement the RTL descriptions are identified, including the size of each signal	Every instruction has an RTL associated with it which seem to do what is required. The components list is complete with signals and their lengths.	(6/6)
Self-consistency	❑ The effect of each individual RTL statement is unambiguous ❑ No state element is assigned more than one value in any given clock cycle	The RTL is interesting and appears to work. The datapath will show this better. The state elements are handled properly.	(6/6)
Demonstration of design principles 1. Simplicity favors regularity 2. Smaller is faster 3. Good design demands good compromises 4. Make the common case fast	❑ Significant delays are balanced between cycles, so that the clock cycle can be as short as reasonably possible ❑ Each instruction uses as few clock cycles as possible without extending the clock cycle ❑ Each component is used efficiently at each clock cycle, and components are not duplicated unnecessarily	Delays are balanced and the length of the RTL is minimized. The components are used appropriately.	(6/6)
Documentation (see below) ❑ Organization ❑ Completeness ❑ Conciseness ❑ Grammar and style	❑ Clear English specifications o The behavior of each component is described unambiguously o Documentation, as listed in the following page, demonstrates all the design issues discussed above	The journal is still in bullet form, please convert it to paragraph form. It flows better and tends to be more in-depth. -1 The memo is still just a cap of what was completed, it doesn't include brief description of decisions made/why they were made. -1 The design document is missing the table of contents. -1 Please add	(13/16)

		one.	
Milestone 1 updates	❑ List of instructions described. ❑ List of special purpose and general purpose registers. ❑ Rules for machine language translation. ❑ Sample programs in assembly and machine language.	This milestone remains in good condition.	(6/6)

Rose-Hulman Institute of Technology
Computer Science and Software Engineering
Prof. Archana Chidanandan

Required Documents

- Memo
  - Objective assessment of design and status
- Design Documentation
  - Demonstration of conceptual understanding
  - Highlights interesting features
- Design Process Journal
  - Alternatives considered
  - Tradeoffs
  - Decisions
- Website