


**Term Project Milestone 2 Evaluation Team # 2-1 Points: ~~28~~/40** 

Evaluation Criteria Categories	Specific Criteria	Comments	Score
Consistency with higher level specifications	<ul style="list-style-type: none"> <li><input type="checkbox"/> Every instruction allowed by the machine language (ML) specification has a unique register transfer language (RTL) representation</li> <li><input type="checkbox"/> The sequences of register transfers specified by each RTL description correctly implement the functions described in the assembly language (AL) specification</li> <li><input type="checkbox"/> Every component referenced in the RTL descriptions is determined</li> <li><input type="checkbox"/> For each component, input, output, and control signals that are sufficient to implement the RTL descriptions are identified, including the size of each signal</li> </ul>	<p>Every instruction has an RTL associated with it.</p> <p>The RTL is wrong for go and goal. Concatenating with the opcode is not right. -1</p> <p>The components are determined.</p> <p>The components have signals but the signals are not given lengths. -1</p>	(4/6)
Self-consistency	<ul style="list-style-type: none"> <li><input type="checkbox"/> The effect of each individual RTL statement is unambiguous</li> <li><input type="checkbox"/> No state element is assigned more than one value in any given clock cycle</li> </ul>	<p>The RTL is done clearly with minor errors.</p> <p>State elements are handled correctly.</p>	(6/6)
Demonstration of design principles 1. Simplicity favors regularity 2. Smaller is faster 3. Good design demands good compromises 4. Make the common case fast	<ul style="list-style-type: none"> <li><input type="checkbox"/> Significant delays are balanced between cycles, so that the clock cycle can be as short as reasonably possible</li> <li><input type="checkbox"/> Each instruction uses as few clock cycles as possible without extending the clock cycle</li> <li><input type="checkbox"/> Each component is used efficiently at each clock cycle, and components are not duplicated unnecessarily</li> </ul>	<p>The delays are handled and the RTL seems minimized. The components are used efficiently.</p>	(6/6)
Documentation (see below) <input type="checkbox"/> Organization <input type="checkbox"/> Completeness <input type="checkbox"/> Conciseness <input type="checkbox"/> Grammar and style	<ul style="list-style-type: none"> <li><input type="checkbox"/> Clear English specifications                             <ul style="list-style-type: none"> <li>o The behavior of each component is described unambiguously</li> <li>o Documentation, as listed in the following page, demonstrates all the design issues discussed above</li> </ul> </li> </ul>	<p>The journal is ok. I see no problems with it at this time.</p> <p>The memo is a bit off. It seems to have been written prior to doing the milestone. It is also not professional looking. -3</p> <p>The design document is missing. It is specified on the site and lower on this grade-sheet. -6</p>	 (6/6)
Milestone 1 updates	<ul style="list-style-type: none"> <li><input type="checkbox"/> List of instructions described.</li> </ul>	<p>Instructions are specified. General purpose are listed, special</p>	(5/6)

	<ul style="list-style-type: none"><li><input type="checkbox"/> List of special purpose and general purpose registers.</li><li><input type="checkbox"/> Rules for machine language translation.</li><li><input type="checkbox"/> Sample programs in assembly and machine language.</li></ul>	purpose are not explained. -1 Machine language is specified. Both programs are converted and given.	
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Required Documents

- Memo
  - Objective assessment of design and status
- Design Documentation
  - Demonstration of conceptual understanding
  - Highlights interesting features
- Design Process Journal
  - Alternatives considered
  - Tradeoffs
  - Decisions
- Website