

Team 1-1 Evaluation  
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For each instruction a clear description is given. I was able to understand what each instruction did as well as quickly figure out what the op code was for each instruction as I was converting my program into machine language. Each instruction has a representation of possible parameters to each instruction. Early in the document each instruction format was clearly represented and easy to understand.

For the branch instructions values inside the registers were used as the address to jump to. This means that the branch instructions had the ability to jump to any spot in memory. The jump instruction lacks detail on how the immediate value is interpreted. I assume that it works similar to MIPS. The branch instructions were easy to use because of the descriptions given and because of the format given.

This document doesn't describe how interrupts are handled nor does it explain which type of I/O handling mechanism is being used. It was obvious that this team used special instructions to handle the inputs and outputs. For these special instructions it was clear which registers it used and what exactly the instruction did.

In this document I don't see any special registers listed. I am assuming that their special registers have the ability to be referenced in a program.

For procedures an instruction exists where control is given to the procedure and so does an instruction that gives control back to the calling procedure.

Overall this document was easy to use as a reference when I was programming. There were some problems. The first problem was that the registers didn't have an associated machine language code translation with them. I assumed the machine code was in the same order as the registers. Second, programming in their language was somewhat difficult when it came to branching or jumping because there were no pseudo instructions for these instructions.