

Term Project Milestone 2 Evaluation Team # 1-6 Points: 35/40

| Evaluation Criteria Categories | Specific Criteria | Comments | Score |
|--|--|--|---------|
| Consistency with higher level specifications | <ul style="list-style-type: none"> <input type="checkbox"/> Every instruction allowed by the machine language (ML) specification has a unique register transfer language (RTL) representation <input type="checkbox"/> The sequences of register transfers specified by each RTL description correctly implement the functions described in the assembly language (AL) specification <input type="checkbox"/> Every component referenced in the RTL descriptions is determined <input type="checkbox"/> For each component, input, output, and control signals that are sufficient to implement the RTL descriptions are identified, including the size of each signal | <p>Every instruction is given an RTL version.</p> <p>The RTL seems to do what it is supposed to.</p> <p>The components are determined for each RTL.</p> <p>The components have signals some of which do not have defined sizes. -1</p> | (5/6) |
| Self-consistency | <ul style="list-style-type: none"> <input type="checkbox"/> The effect of each individual RTL statement is unambiguous <input type="checkbox"/> No state element is assigned more than one value in any given clock cycle | <p>The effects are clear and no state elements are overloaded. They are dealt with correctly.</p> | (6/6) |
| Demonstration of design principles 1. Simplicity favors regularity 2. Smaller is faster 3. Good design demands good compromises 4. Make the common case fast | <ul style="list-style-type: none"> <input type="checkbox"/> Significant delays are balanced between cycles, so that the clock cycle can be as short as reasonably possible <input type="checkbox"/> Each instruction uses as few clock cycles as possible without extending the clock cycle <input type="checkbox"/> Each component is used efficiently at each clock cycle, and components are not duplicated unnecessarily | <p>The delays look balanced and the clock cycles are minimized.</p> <p>The components are used properly and not duplicated unnecessarily.</p> | (6/6) |
| Documentation (see below) <input type="checkbox"/> Organization | <ul style="list-style-type: none"> <input type="checkbox"/> Clear English specifications <ul style="list-style-type: none"> <input type="checkbox"/> The behavior of each | <p>The journal is fine. I see no problems.</p> <p>The memo is also in good condition.</p> | (13/16) |

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| <input type="checkbox"/> Completeness <input type="checkbox"/> Conciseness <input type="checkbox"/> Grammar and style | component is described unambiguously o Documentation, as listed in the following page, demonstrates all the design issues discussed above | The design documents need to be compiled. The one for Milestone 2 should contain everything from 1, and the one for M3 will include M1,2,3. -2 I also requested a table of contents, which is clearly missing. -1 | |
| Milestone 1 updates | <input type="checkbox"/> List of instructions described. <input type="checkbox"/> List of special purpose and general purpose registers. <input type="checkbox"/> Rules for machine language translation. <input type="checkbox"/> Sample programs in assembly and machine language. | Instructions are listed. Registers are defined. ML rules are given. There is only the GCD program, still missing second sample to test all unused instructions. As well as conversion. -1 | (5/6) |

Required Documents

- Memo
 - Objective assessment of design and status
- Design Documentation
 - Demonstration of conceptual understanding
 - Highlights interesting features
- Design Process Journal
 - Alternatives considered
 - Tradeoffs
 - Decisions
- Website