

Term Project Milestone 5 Evaluation Team 1-5 Points 23 /25

Evaluation Criteria Categories	Specific Criteria	Comments	Score
Disciplined Approach to Implementation	<ul style="list-style-type: none"> <input type="checkbox"/> Xilinx components model design specifications. <input type="checkbox"/> Testbenches cover likely modeling errors. <input type="checkbox"/> Level of effort appears to be consistent with completing project on schedule. 	Components include control unit, ALU, registers, multiplexers, sign-extend unit and shifter. Test cases are quite exhaustive and present for all devices.	(9/9)
Documentation (see below) <ul style="list-style-type: none"> <input type="checkbox"/> Organization <input type="checkbox"/> Completeness <input type="checkbox"/> Conciseness <input type="checkbox"/> Grammar and style 	<ul style="list-style-type: none"> <input type="checkbox"/> Detailed integration plan 	Integration plan covers the entire process in sufficient detail. Perhaps a little more detail is required when talking about the special registers. For eg. the PC has a number of possible inputs while the IR only one. Therefore, integration of the PC into the datapath is different from the integration of the IR into the datapath.	(14/16)

Required Documents

- Memo
 - Objective assessment of design and status
- Design Documentation
 - Demonstration of conceptual understanding
 - Highlights interesting features
- Design Process Journal
 - Alternatives considered
 - Tradeoffs
 - Decisions
- Website