

Term Project Milestone 5 Evaluation Team 1-3 Points 19 /25

Evaluation Criteria Categories	Specific Criteria	Comments	Score
Disciplined Approach to Implementation	<ul style="list-style-type: none"> <input type="checkbox"/> Xilinx components model design specifications. <input type="checkbox"/> Testbenches cover likely modeling errors. <input type="checkbox"/> Level of effort appears to be consistent with completing project on schedule. 	Components include control unit, ALU, registers, multiplexers, sign-extend unit and shifter. Test cases are quite exhaustive and present for all devices.	(9/9)
Documentation (see below) <ul style="list-style-type: none"> <input type="checkbox"/> Organization <input type="checkbox"/> Completeness <input type="checkbox"/> Conciseness <input type="checkbox"/> Grammar and style 	<ul style="list-style-type: none"> <input type="checkbox"/> Detailed integration plan 	Integration plan briefly covers handling the PC, IR, memory and the control unit, the ALU and the register file. Does not talk about how the interrupt handling devices will be incorporated. Does not discuss any specific tests that explain how each phase of integration will be tested.	(10/16)

Required Documents

- Memo
 - Objective assessment of design and status
- Design Documentation
 - Demonstration of conceptual understanding
 - Highlights interesting features
- Design Process Journal
 - Alternatives considered
 - Tradeoffs
 - Decisions
- Website