

**Term Project Milestone 5 Evaluation Team   1-2   Points   16  /25**

Evaluation Criteria Categories	Specific Criteria	Comments	Score
Disciplined Approach to Implementation	<ul style="list-style-type: none"> <li><input type="checkbox"/> Xilinx components model design specifications.</li> <li><input type="checkbox"/> Testbenches cover likely modeling errors.</li> <li><input type="checkbox"/> Level of effort appears to be consistent with completing project on schedule.</li> </ul>	Components include control ALU, A and B registers, multiplexers, sign-extend unit, shifter and IR register. Non-exhaustive set of cases present for all devices.	(6/9)
Documentation (see below) <ul style="list-style-type: none"> <li><input type="checkbox"/> Organization</li> <li><input type="checkbox"/> Completeness</li> <li><input type="checkbox"/> Conciseness</li> <li><input type="checkbox"/> Grammar and style</li> </ul>	<ul style="list-style-type: none"> <li><input type="checkbox"/> Detailed integration plan</li> </ul>	Integration plan briefly covers handling the PC, IR, memory and the control unit, the ALU and the register file.	(10/16)

Required Documents

- Memo
  - Objective assessment of design and status
- Design Documentation
  - Demonstration of conceptual understanding
  - Highlights interesting features
- Design Process Journal
  - Alternatives considered
  - Tradeoffs
  - Decisions
- Website