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Computer Science and Software Engineering
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Term Project Milestone 3 Evaluation
(Datapath and Control Specifications) Team _1-2__ Points earned_ /40

Evaluation Criteria Categories	Specific Criteria	Comments	Score
Consistency with higher level specifications	❑ State elements that are assigned or referenced in Register Transfer Language (RTL) statements appear in datapath ❑ Operations that are required to implement RTL statements have corresponding components ❑ Inputs, outputs, and control signals of components in datapath are consistent with RTL specification ❑ Connections between components in datapath are consistent with RTL specification ❑ The control signals specified for each state (or microstep) produce the register transfers specified in the corresponding cycle of the RTL description	State elements appear in the datapath. Components exist for each operation. The inputs and control for status are not there. Input and Output are also not to specification. -1 The connections seem to be done correctly. The FSM seems fine.	(7/8)
Self-consistency	❑ Input signals that have multiple sources have associated multiplexers ❑ Multiplexers have appropriately sized control signals ❑ Datapath includes one or more control units that generate the necessary control signals and have the appropriate input signals ❑ The value of each control signal is defined for every state (or microstep)	There are mux's in these instances. The signals are of proper size for each mux. Control and ALUControl present. Hmmm…..The standard state of each signal is not given. So I don't know what PCin is for any step aside from where it is declared. -1	)

Demonstration of design principles 1. Simplicity favors regularity 2. Smaller is faster 3. Good design demands good compromises 4. Make the common case fast	❑ Components are kept as simple as possible ❑ Similar components used by multiple instructions or in multiple cycles are combined where possible ❑ Tradeoffs between the preceding criteria favor the common case, not the special case ❑ Regularity in the machine language format is exploited by using combinational logic where feasible ❑ Identical states (or microsteps) are combined	Components are simple and minimal. The states seem to be combined where possible.	(8/8)
Documentation (see below) ❑ Organization ❑ Completeness ❑ Conciseness ❑ Grammar and style	❑ Datapath diagram ❑ Clear English specifications o Effects of control signals ❑ Datapath tests ❑ State transition diagram or microprogram specifying the finite state machine ❑ Truth tables or Boolean equations specifying any combinational units ❑ Clear English specifications as necessary ❑ Control unit tests	The datapath is easy to read, and only contains minor problems. The control signals are clearly defined in the FSM and microsteps. Your testing seems to say you will complete the project to make sure this all works. There should be tests you can do before then. -2 The document is missing a table of contents. Please fix this. -2 Everything else looks fine for the design document. Your journal is fine. Your memo isn't as good as I would expect at this point in the project. It contains very little information and is basically a statement of Milestone 3 is done. -2	(10/16)

Required Documents

- Memo
  - Objective assessment of design and status
- Design Documentation
  - Demonstration of conceptual understanding
  - Highlights interesting features
- Design Process Journal
  - Alternatives considered
  - Tradeoffs

    - Decisions
- Website