

Term Project Milestone 5 Evaluation Team 1-1 Points 9 /25

Evaluation Criteria Categories	Specific Criteria	Comments	Score
Disciplined Approach to Implementation	<ul style="list-style-type: none"> <input type="checkbox"/> Xilinx components model design specifications. <input type="checkbox"/> Testbenches cover likely modeling errors. <input type="checkbox"/> Level of effort appears to be consistent with completing project on schedule. 	Testbench only for ALU. -4 Not much effort put into testing any of the components. Insufficient level of effort at this point. Control unit is missing -2	(3/9)
Documentation (see below) <ul style="list-style-type: none"> <input type="checkbox"/> Organization <input type="checkbox"/> Completeness <input type="checkbox"/> Conciseness <input type="checkbox"/> Grammar and style 	<ul style="list-style-type: none"> <input type="checkbox"/> Detailed integration plan 	Integration plan does not cover handling the PC, IR, memory and the control unit. Only the ALU and to some extent the register file.	(6/16)

Required Documents

- Memo
 - Objective assessment of design and status
- Design Documentation
 - Demonstration of conceptual understanding
 - Highlights interesting features
- Design Process Journal
 - Alternatives considered
 - Tradeoffs
 - Decisions
- Website