

Term Project Milestone 2 Evaluation

Evaluation Criteria Categories	Specific Criteria	Comments	Score
Consistency with higher level specifications	<p>Every instruction allowed by the machine language (ML) specification has a unique register transfer language (RTL) representation</p> <p>The sequences of register transfers specified by each RTL description correctly implement the functions described in the assembly language (AL) specification</p> <p>Every component referenced in the RTL descriptions is determined</p> <p>For each component, input, output, and control signals that are sufficient to implement the RTL descriptions are identified, including the size of each signal</p>	<p>Instructions specified in ML have RTL descriptions. Exception: Jump Register and Chain.</p> <p>RTL description seems consistent with specified AL output.</p> <p>Components are, for the most part, well specified. Register File will likely need more input address / output busses.</p> <p>Components mostly have well-specified control signals. Memory file might benefit from the addition of a memread signal.</p>	2/3
Self-consistency	<p>The effect of each individual RTL statement is unambiguous</p> <p>No state element is assigned more than one value in any given clock cycle</p>	<p>RTL statements are clear.</p> <p>Elements are not assigned multiple values in one clock cycle.</p>	3/3
<p>Demonstration of design principles</p> <ol style="list-style-type: none"> 1. Simplicity favors regularity 2. Smaller is faster 3. Good design demands good compromises 4. Make the common case fast 	<p>Significant delays are balanced between cycles, so that the clock cycle can be as short as reasonably possible</p> <p>Each instruction uses as few clock cycles as possible without extending the clock cycle</p> <p>Each component is used efficiently at each clock cycle, and components are not duplicated unnecessarily</p>	<p>Delays are mostly well balanced. Sign-extend is very fast, does not need its own cycle in jump. (why are you sign-extending an address anyway?) Same for Jump Chain.</p> <p>LoadI should use the value in A instead of reading from the register file a second time.</p> <p>Assembler.</p>	3/3

<p>Documentation (see below)</p> <p>Organization</p> <p>Completeness</p> <p>Conciseness</p> <p>Grammar and style</p>	<p>Clear English specifications</p> <ul style="list-style-type: none"> o The behavior of each component is described unambiguously o Documentation, as listed in the following page, demonstrates all the design issues discussed above 	<p>Minor CVS problems with corrupted website / memo.</p> <p>Design Process Documentation should include many of the things considered in the “thoughtsonproject” text file. It should show some of the alternatives that you considered and the reasoning behind you design decisions.</p> <p>Design Documentation provides a clear definition of the RTL used to implement your instructions, as well as a detailed analysis of the components necessary for your instructions.</p> <p>Website is good; links to relevant files and information.</p>	<p>15/16</p>
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Required Documents

- Memo
 - Objective assessment of design and status
- Design Documentation
 - Demonstration of conceptual understanding
 - Highlights interesting features
- Design Process Journal
 - Alternatives considered
 - Tradeoffs
 - Decisions
- Webpage