

ECE 130 HW#8 – Due Monday, March 29

Implement the NAND-NAND circuit from hw#6 in your simulator package. **Print out a copy of your circuit.**

Produce a timing diagram with clocked input signals to prove that it follows the specified truth table.

Print out the diagram, annotate the diagram to illustrate how signal changes propagate through the circuit.

Use your annotations also to **prove that the circuit works correctly** (hint: when are the values high and low?).