Problem 1

Bias the JFET so that 4mA ≤ I_D ≤ 2mA.

Problem 2

Find $V_{DS_{max}}$ and $V_{DS_{min}}$
Problem 3

Find the mid band gain $V_{OL}/V_{OS}$ for the circuit below:

Problem 4

The hybrid-$\pi$ model for a PNP BJT is

Find $V_{OL}/V_{OS}$ for the circuit below: