Problem 1

a) bias the BJT so to allow a ±7 Volt swing
   - specify Icq, VceQ, R1, R2, Rc and Vcc.
   - assume β = 50

b) For your design of Part A, recalculate Icq
   and VceQ using β = 350. For this case, what
   is the symmetrical ± voltage swing?

c) Use PSpice to find VceQ and Icq for
   i) β = 50
   ii) β = 350

   These results should agree with parts a and b
Problem 2

- Use the same values of $I_{CA}$, $V_{CEO}$ and $V_{CC}$ that you used in Problem 1a.

a) Bias the BJT to achieve $I_{CA}$ & $V_{CEO}$. Use $\beta = 50$ design your Ckt such that $(\beta + 1)R_e \gg R_{TH}$ specify $R_1$, $R_2$, $R_E$, $R_C$.

b) Repeat part 1b for your design of part 29

c) Repeat part 1c for this circuit.