Module 2 Due the next class meeting after module 2 is completed in class.

1. **Pipeline**
   a. Name and describe the three primary pipeline stages.
   
   b. Why did TI choose to make program fetches (PF pipeline stage) four cycles in length?
   
   c. Why are pipeline stages of a load (E1-E5) similar to the phases of a program fetch (PG-DP)?
   
   d. What is a Fetch Packet (FP)?
   
   e. What is an Execute Packet (EP)?

2. **Pipeline stalls, insert the proper number of NOPs in the following code:**

   ```
   start:   LDH .D1 *A0, A1
            MPY .M1 A1, A2, A3
            ADD .L1 A1, A2, A4
            LDB .D1 *A5, A6
            LDW .D1 *A7, A8
            MPY .M1 A6, A9, A10
            ADD .L1 A9, A10, A11
            B .S1 start
   ```