Lab 5 Questions

Name: ___________________________________________  Section: ____________

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Read and perform the lab guide posted on the course website. Answer the questions in the lab guide as you get to them in the spaces below. The numbers below refer to steps in the lab guide.

2.1 Examine the exception handler.
   - What is the meaning of “exception level”?
   - What conditions need to be true for an interrupt to occur (your answer should include the bits from the cause and status registers)?

2.3 Trace the execution of exception.asm.
   - When PCSPIM starts are interrupts on or off?
   - What instruction causes the exception and why?
• When the exception is raised:
  (a) What value is stored in the EPC?

  (b) What value is stored in the Cause register?

  (c) What is the value of the “exception level” bit?

• After the exception is processed:
  (a) What instruction is used to return to our program?

  (b) Where in the program do we return?

  (c) Are Interrupts On Or Off?

  (d) What is the value of “exception level”? 