Read and perform the lab guide posted on the course website. Answer the questions in the lab guide as you get to them in the spaces below. The numbers below refer to steps in the lab guide.

1. fib in fib.asm uses $s$ registers to store both $n$ and fib($n - 1$). Could $t$ registers be used? Which would you choose?

2. While calculating fib(4), how many times is fib called?

3. While calculating fib(4), observe the state of the stack when the instruction immediately following the first jal fib in the fib procedure (in my code this is move $s1$, $v0$) is executed for the first time.

Then, draw the state of your stack on the attached sheet. Each rectangle on the stack diagram represents a single stack frame. Your answer should the name of the item on the stack and its value.

4. What happens if fib does not restore the return address before using jr?