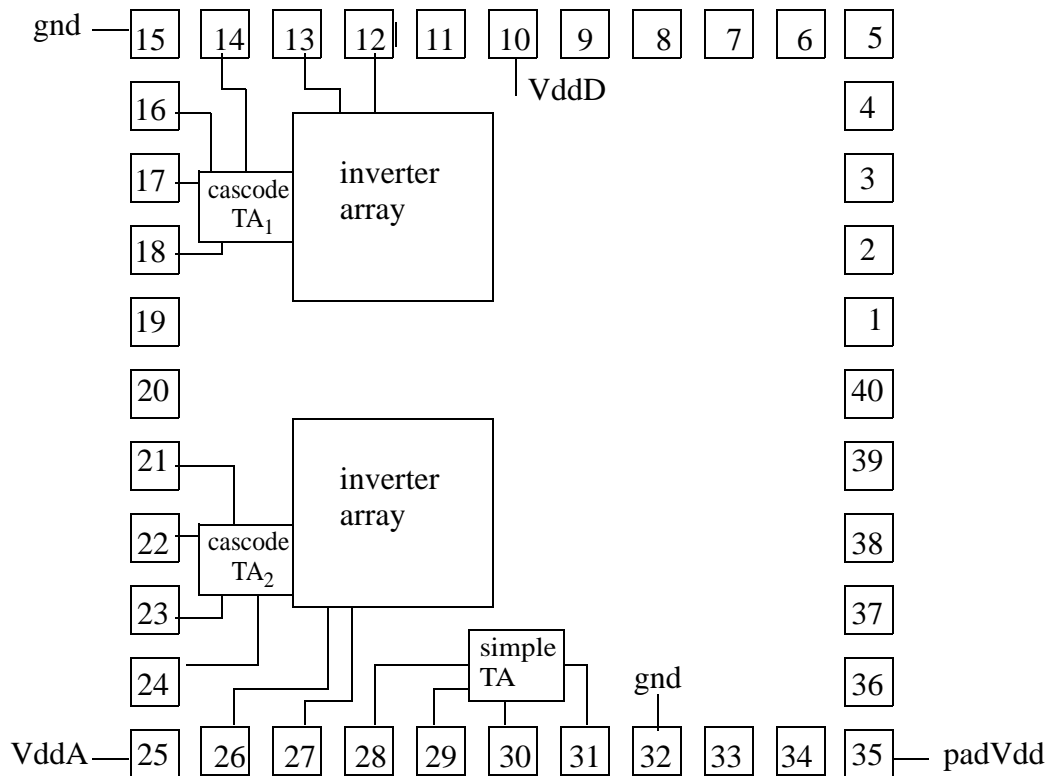


PinOut



Pin List

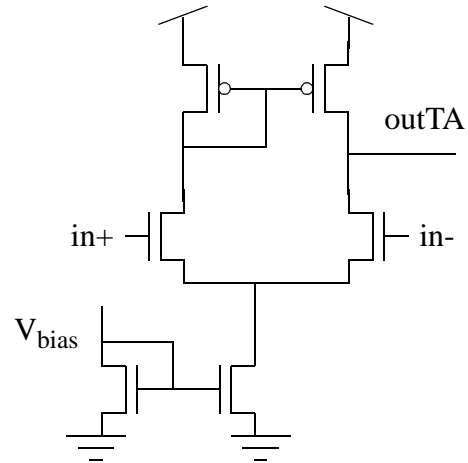
1: X	11: X	21: outTA ₂	31: outTA
2: X	12: digitalOut ₁	22: in-	32: gnd
3: X	13: digitalIn ₁	23: in+	33: X
4: X	14: outTA ₁	24: Vbias	34: X
5: X	15: gnd	25: VddA	35: padVdd
6: X	16: in-	26: digitalOut ₂	36: X
7: X	17: in+	27: digitalIn ₂	37: X
8: X	18: Vbias	28: in+	38: X
9: X	19: X	29: Vbias	39: X
10: VddD	20: X	30: in-	40: X

Notes:

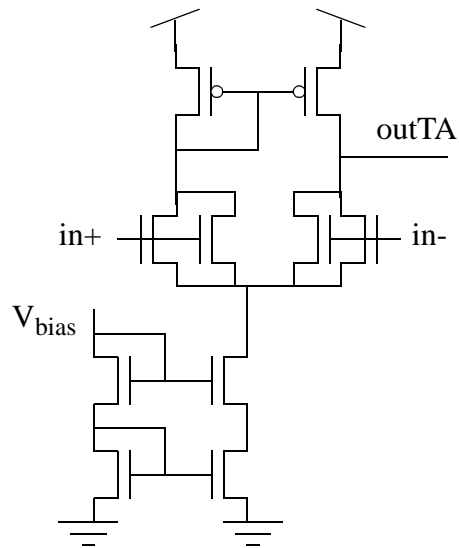
- *padVdd* (35), *VddD* (10), and *VddA* (25) may be connected together.
- X denotes no connection.
- all *in-* (16, 22, 30) inputs may be connected together
- all *in+* (17, 23, 28) inputs may be connected together
- all *Vbias* (18, 24, 29) inputs may be connected together
- If the inverter chain is driven by an external function generator as opposed to being used as a ring oscillator, *digitalIn₁* and *digitalIn₂* may be connected together.

Schematics

Simple TA



Cascode TA₁ and Cascode TA₂



Inverter Array

