

Interrupt Priority Feature

Set Interrupt Mode	RCONbits.IPEN	Compatibility (One ISR)	Priority (High and Low)
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Global / High Priority Bit

Global Interrupt	INTCONbits.GIE	All interrupts off	Globally turn on interrupts
High Priority Interrupt	INTCONbits.GIEH	All interrupts off	High Priority on

Peripheral / Low Priority Bit

Peripheral Interrupt	INTCONbits.PEIE	Peripherals off	Peripheral interrupts on
Low Priority Interrupt	INTCONbits.GIEL	Low Priority off	Low Priority on

Interrupt on Change

- On RB4,RB5,RB6,RB7 (KB0,KB1,KB2,KB3)

Enable	INTCONbits.RBIE	off	Turn on RB4:7 interrupts
Flag	INTCONbits.RBIF	-	Interrupt occurred!
Priority	INTCON2bits.RBIP	Low Priority	High Priority

Interrupt 0 - On RBO (INT0)

Enable	INTCONbits.INT0IE	off	Turn on INT 0 interrupts
Flag	INTCONbits.INT0IF	-	Interrupt occurred!
Edge	INTCON2bits.INTEDG0	Falling Edge	Rising Edge

Interrupt 1 - On RB1 (INT1)

Enable	INTCON3bits.INT1IE	off	Turn on INT 1 interrupts
Flag	INTCON3bits.INT1IF	-	Interrupt occurred!
Priority	INTCON3bits.INT1IP	Low Priority	High Priority
Edge	INTCON2bits.INTEDG1	Falling Edge	Rising Edge

Interrupt 2 - On RB2 (INT2)

Enable	INTCON3bits.INT2IE	off	Turn on INT 2 interrupts
Flag	INTCON3bits.INT2IF	-	Interrupt occurred!
Priority	INTCON3bits.INT2IP	Low Priority	High Priority
Edge	INTCON2bits.INTEDG2	Falling Edge	Rising Edge

Timer 0 Interrupt

Enable	INTCONbits.TMR0IE	off	Turn on Timer 0 interrupts
Flag	INTCONbits.TMR0IF	-	Interrupt occurred!
Priority	INTCON2bits.TMR0IP	Low Priority	High Priority

Timer 1 Interrupt

Enable	PIE1bits.TMR1IE	off	Turn on Timer 1 interrupts
Flag	PIR1bits.TMR1IF	-	Interrupt occurred!
Priority	IPR1bits.TMR1IP	Low Priority	High Priority

Timer 2 Interrupt

Enable	PIE1bits.TMR2IE	off	Turn on Timer 2 interrupts
Flag	PIR1bits.TMR2IF	-	Interrupt occurred!
Priority	IPR1bits.TMR2IP	Low Priority	High Priority

Timer 3 Interrupt

Enable	PIE2bits.TMR3IE	off	Turn on Timer 3 interrupts
Flag	PIR2bits.TMR3IF	-	Interrupt occurred!
Priority	IPR2bits.TMR3IP	Low Priority	High Priority