

ECE203-02 DC CIRCUITS

Course Outline

Electrical and computer engineering (ECE) deals with the application of electrical and electronic technology to the daily needs of people. Almost every aspect of their lives and occupations are controlled by ECE. To understand the details of ECE it is necessary to study circuit analysis. The reason for this is that it is the one thing that ties all branches of ECE together; as such, it lays the foundation for analysis, design, and operation of electrical devices and systems.

This class helps provide a foundation for you to build on. You will continue, in subsequent ECE courses, to update the knowledge you gain here. Since this course is the prerequisite for just about everything you will be doing in ECE, it is vital that you gain a confident grasp of circuit and systems fundamentals.

Instructor: Cliff Grigg
Offices: D 206
Phone: 8333
Required Text: ELECTRIC CIRCUITS (10th Ed. Abridged) – Nilsson & Riedel
Grading: Grades shall be assigned according to the following schedule, which will not be curved:

90	100	A		
85	90	B+		
80	85	B	Final Exam	40%
75	80	C+	Tests (3)	30%
70	75	C	Lab Work	20%
65	70	D+	HW & Quizzes	10%
60 - 65		D		
Below 60		F		

NOTE: All Labs must be satisfactorily completed by each student to pass the course. Anyone missing a lab will be required to complete it on their own within one week's time. For unexcused absences, the completed lab will be penalized by 25% per day. All labs must be completed even if the grade is a zero.

The link for the course webpage which will have homework hints, test reviews, and supplemental material is: <http://www.rose-hulman.edu/class/ee/HTML/ECE203/>
A Moodle page is also being developed.

Homework is assigned daily and collected on the day after it is assigned. **Completed homework assignments are due at the START of the class period indicated in the schedule.** Drop your assignment on the instructor's bench as you enter the class. Late assignments will be accepted with a 25% penalty for the rest of that school day. Assignments turned in after the due date will not be accepted unless written exemption has been obtained in advance. Turn in your homework early if you will be away for job interviews, athletic events, etc. You are expected to be able to solve linear equations and to perform basic complex algebra on your calculator. Laptops should only be used to check your results outside of class, as they will not be available for the tests. Students are encouraged to do homework together and to study together, bearing in mind that outright copying of homework solutions is counter-productive to learning the material.

Homework will be graded using a restricted scale of 0, 1, 2, 3, 4, as follows:

- 4 – Problem worked completely correctly.
- 3 – Problem worked completely, but with minor errors.
- 2 – Problem worked completely, but with substantial errors.
- 1 – Problem attempted, but with little understanding.
- 0 – Problem not attempted, or attempted with no understanding.

All students are expected to attend all lectures unless they have a valid reason for absence. Illness is not an acceptable reason for absence unless accompanied by medical certification. **Tests missed due to unauthorized absence may not be made-up.** Tests missed due to authorized absence will receive the final exam score. A student whose total absences (excused or unexcused) exceeds eight class meetings shall fail the course.

Each test and the final exam must be the student's own work. Failure to comply with this requirement results in an automatic course failure for all parties concerned, and recommendation for further disciplinary action. **NO SECOND CHANCES!!!** Although students are encouraged to study together and help each other outside the classroom, this does not extend to tests and the final exam.

On both tests you will be allowed to use one 8½ x 11 page of notes (both sides) that you have made but may not use anyone else's notes. However, no time will be allocated for referring to notes etc. In other words, you should prepare for tests as though they were **closed-book** so that you can finish on-time; reference material should only be a back-up. In the final exam, you are permitted to use two 8½ x 11 pages of notes (both sides).

Quizzes will usually be administered at the start of the lab period; consequently arriving late for lab will seriously affect your course grade. Quizzes missed due to authorized absence will be made-up by the final exam grade; quizzes missed due to unauthorized absence will receive a grade of zero. Each laboratory group is required to keep two laboratory notebooks (#26-251, available at the Bookstore). One book shall contain the odd-numbered labs and the other shall contain the even-numbered labs. The pre-lab must be present in the lab book and a photocopy of the pre-lab is due at the start of the lab. Drop your pre-lab on the instructor's bench as you enter the class. The required format for homework and lab books is described in the Laboratory Manual.

READING ASSIGNMENTS

Reading is a very important part of learning the material, simply working examples produces **superficial** learning, while reading produces **in-depth** learning. The following is a target schedule for the course. Read the assigned material **before** it is covered in class; don't worry if you don't understand it when you first read it, this will get you ready to ask questions in class and to learn from other people's questions. If you don't understand the material **after** it is covered in class **come and see your instructor**. The **Drill Problems** will provide you with helpful practice; work them on your own or in groups after you have done the reading assignment and if you have questions **come and see your instructor**.

COURSE SCHEDULE

Week-Day	Date	Text	Topic
1-1	3/06	1.1 - 1.3	Introduction to ECE, SI Units, Overview
1-2	-	1.4 - 1.6	Voltage, Current, Power, and Energy Definitions, Passive Sign Convention
1-3	-	2.1 - 2.3	Voltage Source, Current Source, Resistors, Ohm's Law, and Power
2-1	3/13	2.4 - 2.5	Kirchhoff's Laws
2-2	-	2.4 - 2.5	Application of Kirchhoff's Laws
2-3	-	3.1 - 3.2	Resistors in Series and Parallel
3-1	3/20	3.3 - 3.5	Voltage and Current Divider Circuits
3-2	-	3.6 - 3.7	Wheatstone Bridge & Wye-Delta Equivalents
3-3	-	Chaps. 1 - 3	Test No. 1
4-1	3/27	4.1 - 4.2	Circuit Analysis – Definitions and Node Voltage Method
4-2	-	4.3 - 4.4	Node Voltage Method and Dependent Sources
4-3	-	4.5	Mesh Current Method
5-1	4/03	4.6 - 4.7	Mesh Current Method and Dependent Sources
5-2	-	4.9	Source Transformations
5-3	-	4.10	Thevenin and Norton Equivalents
6-1	4/17	4.11	Thevenin and Norton Equivalents
6-2	-	4.12	Maximum Power Transfer
6-3	-	3.7 - 4.11	Test No. 2
7-1	4/24	4.13	Superposition
7-2	-	5.1 - 5.2	Introduction to Operational Amplifiers
7-3	-	5.3 - 5.4	Inverting and Summing Op Amp
8-1	5/01	5.5 - 5.6	Noninverting and Difference Op Amp
8-2	-	5.7	Cascading & Realistic Op Amp Models
8-3	-	Hand-out	Op-amp Design and Applications
9-1	5/08	6.1 - 6.2	Inductors & Capacitors
9-2	-	7.1	Natural Response of an RL Circuit
9-3	-	4.12 - 5.7	Test No. 3
10-1	5/15	7.2	Natural Response of an RC Circuit
10-2	-	7.3	Step Response of RL & RC Circuits
10-3	-	AOTA	Review
Final	-	AOTA	Final exam